Universal Serial Bus 4 (USB4™)
Router Assembly
Electrical Compliance Test Specification

Date: June 2020

Revision: 1.0

Revision History:

Revision	Issue Date	Comments
1.0	June 2020	First Release

CONTENTS

Table of Figures

Acronyms

Acronym	Definition
BER	Bit Error Rate
BW	Band Width
CDR	Clock Data Recovery
DUT	Device Under Testing
NVM	Non Volatile Memory
PJ	Periodic Jitter
SSC	Spread Spectrum Clock
TJ	Total Jitter
UI	Unit Interval
UJ	Uncorrelated Jitter
UDJ	Uncorrelated Deterministic Jitter
RJ	Random Jitter
ISI	Inter Symbol Interference
DDJ	Data dependent Jitter
PPM	Parts Per Million
PRBS	Pseudo Random Bit Sequence
IL	Insertion Loss

1 Introduction

This document contains the specification of procedures, tools and criteria for testing electrical compliance of Router Assembly according to the Universal Serial Bus 4 (USB4™) Specification, Version 1.0.

2 Test Equipment

2.1 Overview

All equipment used for testing shall comply with the requirement lists specified below.

This includes the primary equipment that is used in the USB4 authorized test centers and may also be used by the end user for self-testing.

Other configurations and equipment may be used for self-testing as long as that equipment and the processes meet all of the stated and implied requirements and permit an equivalent level of testing.

2.2 Test Equipment Requirements

All test equipment requires calibration to ensure accurate and repeatable results. Equipment shall be calibrated prior to, and if necessary, during the test procedure.

2.2.1 Test Point Access Boards

To gain access to the required signals, a variety of test point access boards are required. Test Point Access boards provide test points for the pins on the USB4 USB Type-C connector and an easy way to control the DUT.

<u>Recommended Test Equipment:</u>

1. Wilder USB4 Plug and receptacle Test fixtures.
2. Wilder USB4 Micro-Controller Test Module with USB cable.
3. Control PC \ Scope running the latest USB4 SW Electrical Test Tool (ETT).

2.2.2 Real Time Scopes

<u>Required Test Equipment Capabilities:</u>

- DC to 21GHz, -3db bandwidth or greater
- 80G sample/sec Sampling rate or greater, sampling 2 channels simultaneously
- Sample memory: 50M samples per channel or greater
- 1st and 2nd order CDR capability
- Equalization of CTLE and DFE
- Jitter measurement
- Embedding/de-embedding capability
- High impedance probe

2.2.3 Pattern Generator

Generate USB4 signal with a variety of patterns, jitter injection capability.

Required Test Equipment Capabilities:

- Data rates ≥ 20Gbps
- Data patterns: PRBS15, PRBS31, Square wave
- Differential swing range: 0 – 2Vp-p in 10mV steps
- Rise Time ≥ 10 ps (20%-80%)
- Intrinsic jitter ≤ 400 fS RMS
- Random Jitter profile no smaller than 500MHz
- Equalization
- SSC spread deviation from 0.03% down to -0.53%

  Note: *For Interoperability with Thunderbolt™ 3 (TBT3) Systems the SSC spread deviation shall be from 0.04% down to -0.54%.*

- Injection of 1 SJ sources

2.2.4 Network Analyzer

Required Test Equipment Capabilities:

- 2 ports used simultaneously
- At least 1MHz – 13GHz bandwidth
- Dynamic range > 50db
- Time domain option

2.2.5 Signal Generator

Required Test Equipment Capabilities:

- Frequency range of at least 10- 400MHz
- Output power of ±15dBm

2.2.6 Accessories

2.2.6.1 Low Insertion Loss Phase Matched Cable:

Required Test Equipment Capabilities for 1m RF cable:

- Phase matched max of±5º @ 40 GHz
- Max IL in 10GHz < 1.5dB

Note: *Any length of cable is permitted up to 1m.*

2.2.6.2 ISI Channel

Required Test Equipment Capabilities:

Varied insertion loss of 2-10dB @5GHz and @10GHz

2.2.6.3 Pickoff Tee

Required Test Equipment Capabilities:

Pickoff tees with bandwidth of at least 40GHz

2.2.6.4 Splitter

Required Test Equipment Capabilities:

Splitter with bandwidth of at least 1GHz

2.2.6.5 DC Block

Required Test Equipment Capabilities:

- DC Blocks with bandwidth of at least 33GHz
- 1 dB Insertion Loss to 40 GHz
- Capacitance of 135nF-265nF (220nF is recommended)

2.2.6.6 USB4 Passive and Active Cables

- USB Type-C 0.8m (Gen3 supported) and 2m (Gen2 supported) passive cables as described in the USB4 Specification. The cables should be e-marked.
- USB4 Active cable. The Active cable should be e-marked.

2.2.6.7 Termination

Required Test Equipment Capabilities:

- Impedance 50 ohm
- Frequency DC to 20 GHz

3 Router Assembly Transmitter Testing

- Router Assembly transmitter compliance testing is defined at the output of a “golden” plug fixture at the TP2 reference point and at the output of a “golden” receptacle fixture at the TP3 reference point.
- TP2 - Reference measurement point located at the USB Type-C plug connected to the Router Assembly TX output. Used as a reference point for defining Router Assembly TX.
- TP3- Reference measurement point located at USB Type-C receptacle output on the far-end side of passive cable. Used as a reference point for passive installations. All the measurements at this point shall be done while applying reference equalization.
- All measurements shall be referenced to the TP2/TP3 compliance point.
- Calibration shall be applied in cases where direct measurement is not feasible.
- All jitter measurements are referenced to the average frequency.

The following sections provide detailed information on the setup and testing of the USB4 parameters. In the event of a discrepancy, the USB4 Specification prevails.

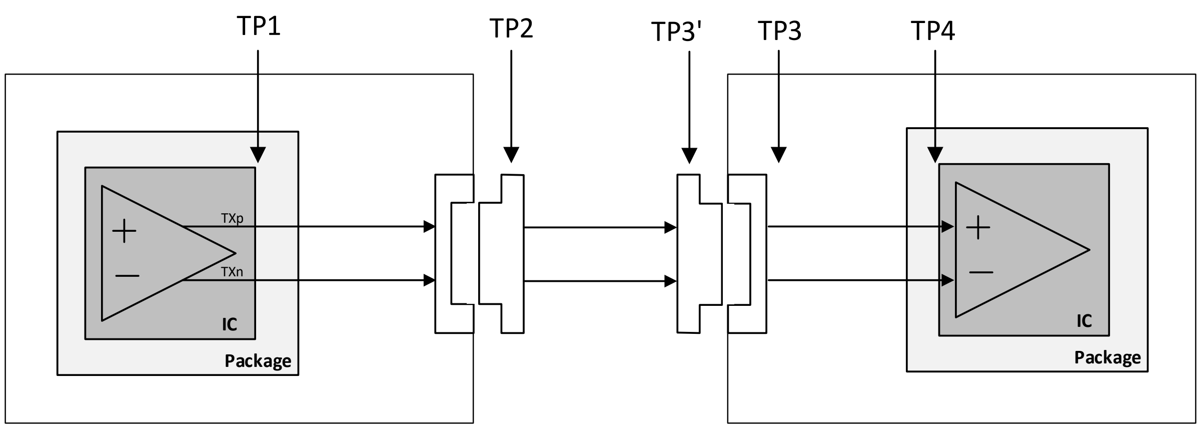


Figure 1. USB4 TX Compliance Points Definition

3.1 Transmitter Test Setup

The following figure shows the connections to the DUT and control PC used for Transmitter testing. The last RF cables from the USB Type-C plug to the scope shall be de-embedded.

Note: *Before beginning any test or data acquisition, the Oscilloscope must be warmed, and calibrated. The cables used for measurement shall be de-embedded (4 port). For all tests the signal must be vertical tuned to be opened.*

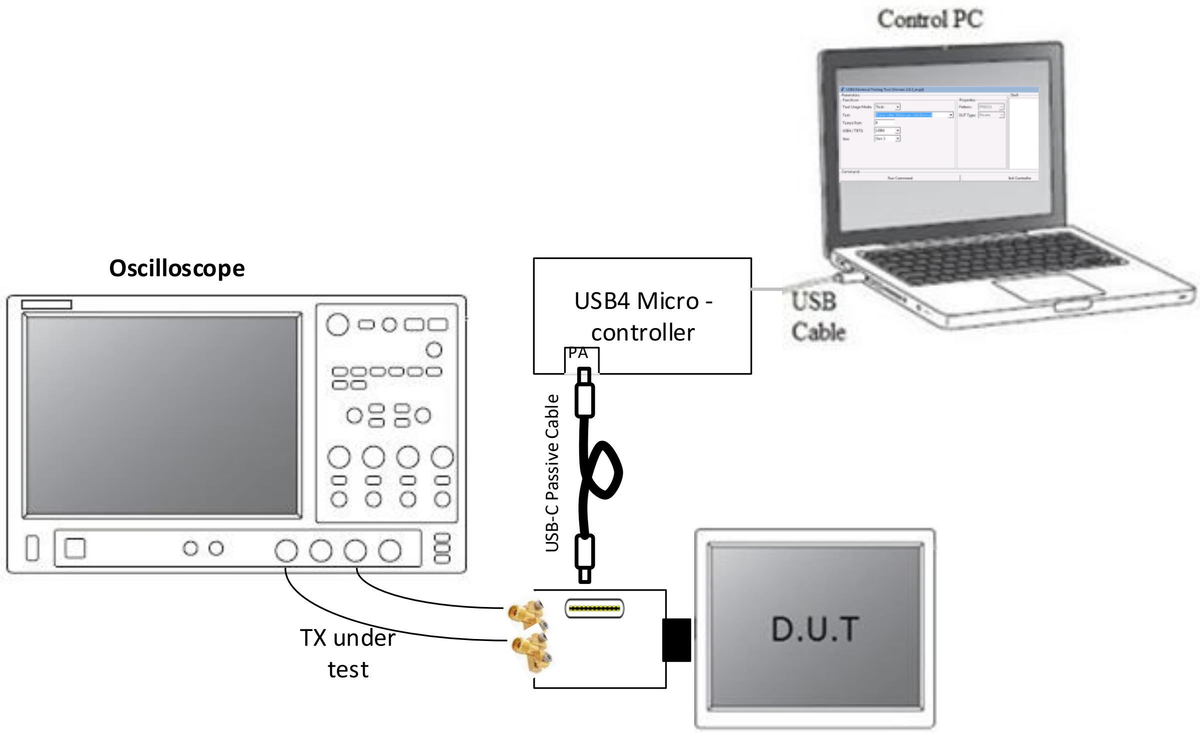


Figure 2. Transmitter TP2/TP3 Test Setup

3.2 Connecting to the DUT

1. Connect Lane under test TX_P, TX_N to the Oscilloscope.
2. Connect the Low speed signals from the USB4 test Fixture to the USB4 Micro-controller PA using a USB Type-C passive cable.
3. The USB4 Micro-controller is connected to control PC via USB cable, running the latest USB4 SW Electrical Test Tool (ETT).

Note: *the Transmitter Router Assembly is tested with the default preset. It is recommended that the default preset will be the one that causing the transmitted signal to be with minimum DDJ. See Appendix G.*

3.3 Gen2 Router Assembly Transmitter Compliance

***Note**: Refer to Sections 3 - 3.2 for test Setup.*

3.3.1 Gen2 Transmitter Equalization

3.3.1.1 Reference

USB4 Specification Table 3-3 and 3-5.

Preset Number	Pre-shoot [dB]	De-emphasis [dB]	Informative Filter Coefficients		
			C_{-1}	C_0	C_1
0	0	0	0	1	0
1	0	-1.9	0	0.90	-0.10
2	0	-3.6	0	0.83	-0.17
3	0	-5.0	0	0.78	-0.22
4	0	-8.4	0	0.69	-0.31
5	0.9	0	-0.05	0.95	0
6	1.1	-1.9	-0.05	0.86	-0.09
7	1.4	-3.8	-0.05	0.79	-0.16
8	1.7	-5.8	-0.05	0.73	-0.22
9	2.1	-8.0	-0.05	0.68	-0.27
10	1.7	0	-0.09	0.91	0
11	2.2	-2.2	-0.09	0.82	-0.09
12	2.5	-3.6	-0.09	0.77	-0.14
13	3.4	-6.7	-0.09	0.69	-0.22
14	3.8	-3.8	-0.13	0.74	-0.13
15	1.7	-1.7	-0.05	0.55	-0.05

Transmit Equalization Presets Table

3.3.1.2 Requirement

Transmitter swing 3.5 ± 1dB (for preset 15 only).

Pre-shoot, de-emphasis ± 1dB for the Presets specified in the Transmit Equalization Preset Table above.

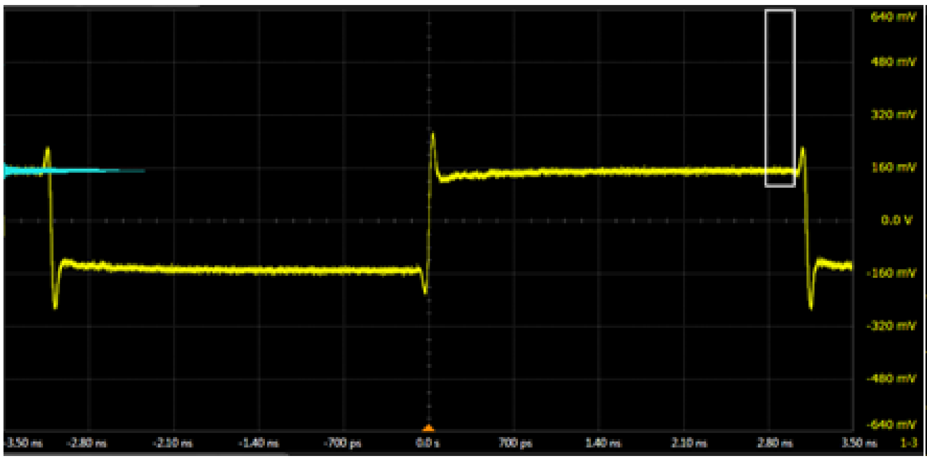
3.3.1.3 Test Objective

Confirm that the transmitter equalization falls within the limits of the USB4 Specification.

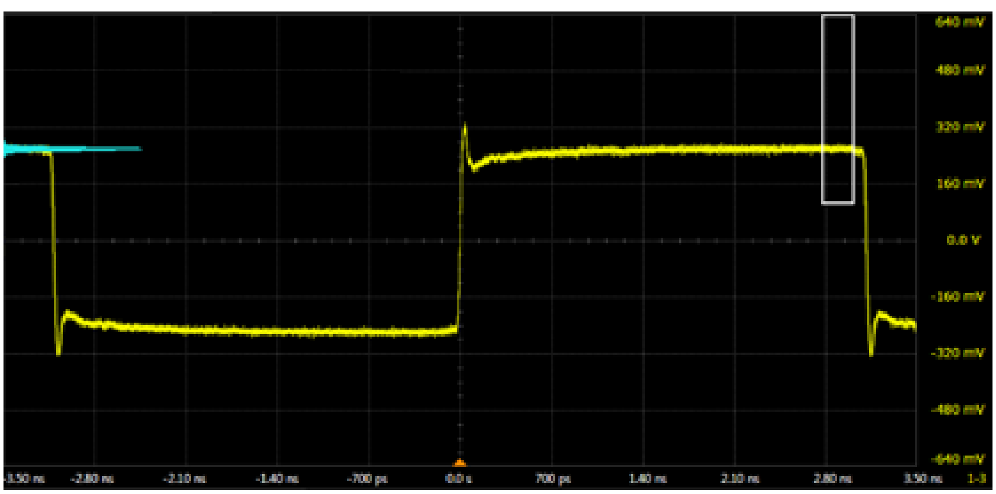
3.3.1.4 Test Method

1. Set preset 0.
2. Choose a supported USB4 Gen2 speed.
3. Scope BW shall be as specified in 6.1.

4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Configure DUT transmitter to output alternating square pattern of 64 0’s and 64 1’s on all lanes with SSC turned on with pre-shoot and with de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with pre-shoot and with de-emphasis based on local clock without SSC modulation on all lanes.
6. Adjust vertical scale to fit signal into scope screen.
7. Use average of 150 cycles; no CDR and no interpolation to be used.



8. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_1.
9. Configure DUT transmitter to output alternating square pattern of 64 0’s and 64 1’s on all lanes with SSC turned on with no pre-shoot and with de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with de-emphasis based on local clock without SSC modulation on all lanes.
10. Adjust vertical scale to fit signal into scope screen.
11. Use average of 150 cycles; no CDR and no interpolation to be used.

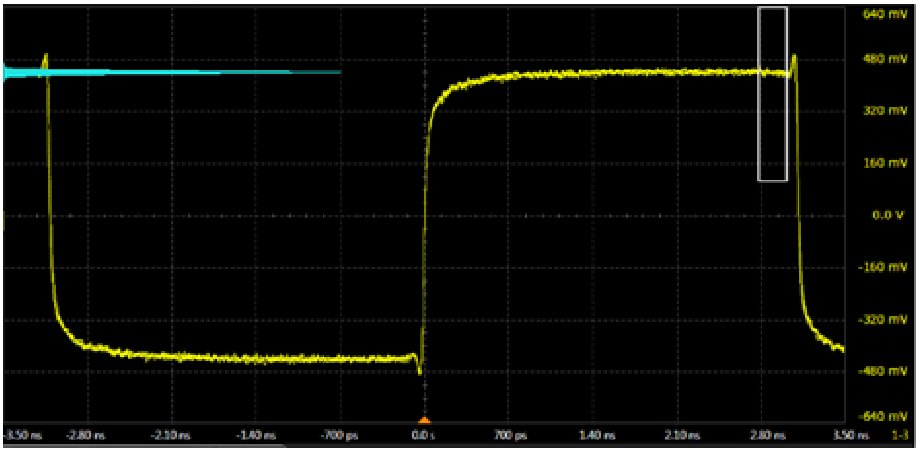


12. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_2.

13. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with pre-shoot but with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.

14. Adjust vertical scale to fit signal into scope screen.

15. Use average of 150 cycles; no CDR and no interpolation to be used.



16. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_3.

17. Set Pre-shoot to be $20 * log_{10} {}^{V_2}/_{V_1}$.

18. Set De-emphasis to be $20 * log_{10} {}^{V_1}/_{V_3}$.

19. If Pre-shoot is not within ± 1dB of the matching value in the Transmit Equalization Preset Table then **Fail**.

20. If De-emphasis is not within ± 1dB of the matching value in the Transmit Equalization Preset Table then **Fail**.

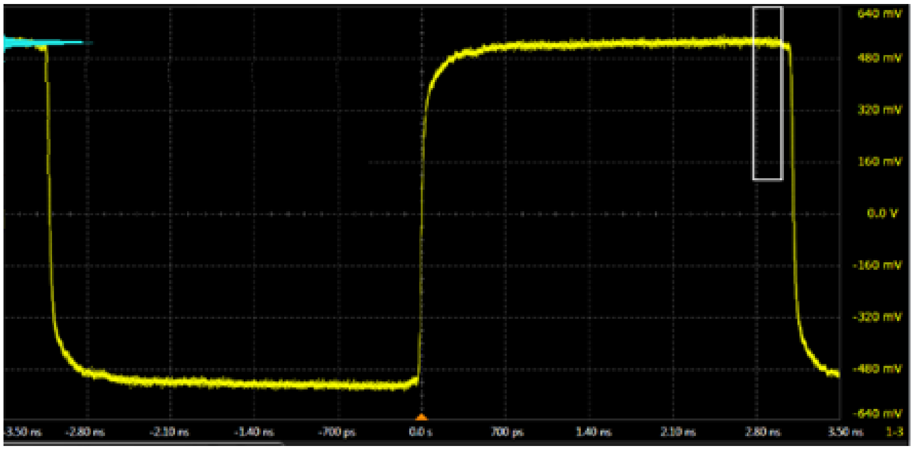
21. Repeat for all Presets in the Transmit Equalization Preset Table.

22. Set the DUT to Preset 0.

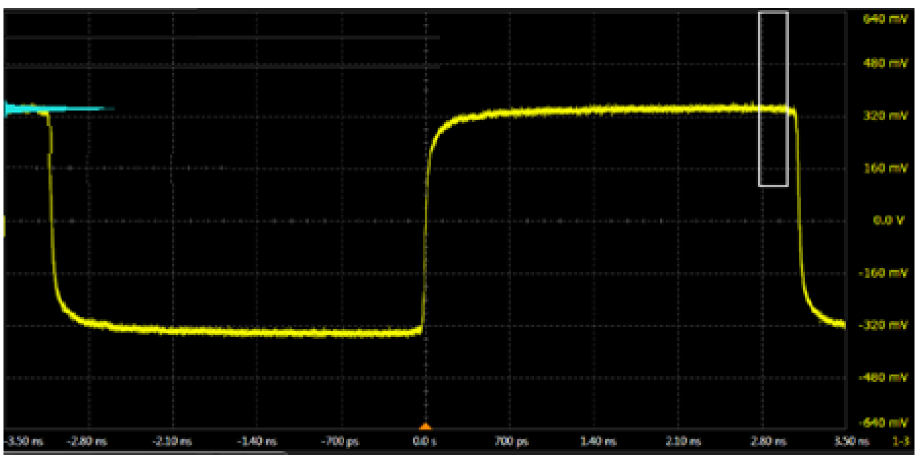
23. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with no pre-shoot and with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.

24. Adjust vertical scale to fit signal into scope screen.

25. Use average of 150 cycles; no CDR and no interpolation to be used.



26. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_0.
27. Set the DUT to Preset 15.
28. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with no pre-shoot and with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.
29. Adjust vertical scale to fit signal into scope screen.
30. Use average of 150 cycles; no CDR and no interpolation to be used.



31. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_{15}.
32. Set Swing to be $20 * log_{10} {}^{V_0}/_{V_{15}}$.
33. If Swing < 2.5 dB or Swing > 4.5 dB then **Fail**.
34. Repeat the test for all remaining USB4 lanes.

3.3.2 Gen2 Minimum Unit Interval Measurement

3.3.2.1 Reference

UI - USB4 Specification Table 3-6.

3.3.2.2 Requirement

99.97ps ≤ Minimum Unit Interval ≤ 100.03ps

3.3.2.3 Test Objective

Confirm that the Minimum Unit Interval under all conditions does not exceed minimum or maximum limits of the USB4 Specification during steady-state.

3.3.2.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. Minimum UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 3000 symbols.
6. Capture 1 measurement and calculate both Min and Max of Minimum UI values.
7. If Max Minimum UI > 100.03ps **or** Min Minimum UI < 99.97ps then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.3 Gen2 SSC Down Spread Range Measurement

3.3.3.1 Reference

SSC_Down_Spread_Range - USB4 Specification Table 3-3.

3.3.3.2 Requirement

0.4% ≤ SSC_Down_Spread_Range ≤ 0.5%.

3.3.3.3 Test Objective

Confirm that the Dynamic range of SSC down-spreading during steady-state falls within the limits of the USB4 Specification.

3.3.3.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 3000 symbols.
6. Capture values of Max(maximum UI), Min(maximum UI), Max(minimum UI) and Min(minimum UI). calculate the Max and Min value of SSC_Down_Spread_Range:
    - Max(Max Deviation)= 100*((Bit_Rate - (1/ Max(maximum UI)))/ Bit_Rate).
    - Min(Min Deviation)= 100*((Bit_Rate - (1/ Min(minimum UI)))/ Bit_Rate).
    - Min(Max Deviation)= 100*((Bit_Rate - (1/ Min(maximum UI)))/ Bit_Rate).
    - Max(Min Deviation)= 100*((Bit_Rate - (1/ Max(minimum UI)))/ Bit_Rate).
    - Bit_Rate is the base frequency with 0ppm.
    - Max SSC_Down_Spread_Range = Max(Max Deviation) – Min(Min Deviation).
    - Min SSC_Down_Spread_Range = Min(Max Deviation) – Max(Min Deviation).
7. If Max SSC_Down_Spread_Range > 0.5% **or** Min SSC_Down_Spread_Range < 0.4% then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.4 Gen2 SSC Down Spread Rate Measurement

3.3.4.1 Reference

SSC_Down_Spread_Rate - USB4 Specification Table 3-3.

3.3.4.2 Requirement

30KHz ≤ SSC_Down_Spread_Rate ≤ 33KHz.

3.3.4.3 Test Objective

Confirm that the SSC down-spreading modulation rate during steady-state falls within the limits of the USB4 Specification.

3.3.4.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
   - No CDR, no average and no interpolation to be used.
   - Adjust vertical scale to fit signal into scope screen.
   - Scope BW shall be as specified in 6.1.
5. UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 3000 symbols.
6. Capture values of Max SSC_Down_Spread_Rate and Min SSC_Down_Spread_Rate.
7. If 30KHz > Min SSC_Down_Spread_Rate **or** Max SSC_Down_Spread_Rate > 33KHz then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.5 Gen2 SSC Phase Deviation Measurement

3.3.5.1 Reference

SSC_Phase_Deviation - USB4 Specification Table 3-3.

3.3.5.2 Requirement

2.5ns p-p ≤ SSC_Phase_Deviation ≤ 22ns p-p.

3.3.5.3 Test Objective

Confirm that the Phase jitter associated with the SSC modulation during steady-state falls within the limits of the USB4 Specification.

3.3.5.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and post process it with an appropriate software:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The SSC Phase deviation shall be extracted from the transmitted signal.
    - The SSC Phase deviation shall be extracted from the signal phase after applying a 2nd order Low-Pass-Filter (LPF) with 3dB point at 5MHz.
    - Scope BW shall be as specified in 6.1.
5. If SSC_Phase_Deviation < 2.5ns p-p or SSC_Phase_Deviation > 22ns p-p then **Fail.**
6. Repeat the test for all remaining USB4 lanes.

3.3.6 Gen2 SSC Slew Rate Measurement

3.3.6.1 Reference

SSC_Slew_Rate - USB4 Specification Table 3-3.

3.3.6.2 Requirement

SSC_Slew_Rate ≤ 1250 ppm/µs.

3.3.6.3 Test Objective

Confirm that the SSC Slew Rate during steady-state falls within the limits of the specification.

3.3.6.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and post process it with an appropriate software:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The SSC slew rate shall be extracted from the transmitted signal over measurement intervals of 0.5µs.
    - The SSC slew rate shall be extracted from the signal phase after applying a 2nd order Low-Pass-Filter (LPF) with 3dB point at 5MHz.
    - Scope BW shall be as specified in 6.1.
    - SSC_SLEW_RATE measurement shall be done in steady-state after the Link training phase is completed.
5. If SSC_Slew_Rate > 1250 ppm/µs then **Fail.**
6. Repeat the test for all remaining USB4 lanes.

3.3.7 Gen2 TX Frequency Variation Training Measurement

3.3.7.1 Reference

TX_FREQ_VARIATIONS_TRAINING – USB4 Specification Table 3-3.

3.3.7.2 Requirement

TX_FREQ_VARIATIONS_TRAINING:
-300 ≤ INIT_FREQ_VARIATION ≤ 300 ppm
DELTA_FREQ_200ns ≤ 1400 ppm
DELTA_FREQ_1000ns ≤ 2200 ppm

3.3.7.3 Test Objective

Confirm that the Frequency variation during Link training falls within the limits of the specification.

Note: *This test shall be applied only when the DUT contains Re-Timer.*

3.3.7.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. For emulating link training, DUT shall be configured such that the Router transmitting PRBS31 with SSC modulation on all lanes.
3. The Re-Timer placed near the USB Type-C connector shall be configured to transmit SQ128 based on local clock without SSC modulation on all lanes. If additional Re-Timer exist in the DUT, it shall be configured to transmit SQ4 based on local clock without SSC modulation on all lanes.
4. Initiate clock switch from local clock to recovered clock for the Re-Timer placed near the USB Type-C connector.
5. The cables from the plug test fixture to the scope shall be de-embedded.
6. Capture the clock switch period during three stages: pre-clock switch, clock switch and post-clock switch in a single waveform and post process it as following:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The frequency variation shall be performed over the transmitted signal.
    - The signal phase shall be extracted after applying a 2nd order Low-Pass-Filter (LPF) with 3dB point at 5MHz.

- Calculate the initial non-modulated transmit frequency aka INIT_FREQ_VARIATION.
- Calculate the frequency variation during Link training (switch clock) over 200ns measurement windows aka DELTA_FREQ_200ns.
- Calculate the frequency variation during Link training (switch clock) over 1000ns measurement windows aka DELTA_FREQ_1000ns.
- Scope BW shall be as specified in 6.1.

7. If additional Re-Timer exist in the DUT, then the 2nd clock switch need to be analyzed as well from the saved waveform in step 6 above.
8. Repeat this procedure 20 times and report the worst captured waveform.
9. If INIT_FREQ_VARIATION > 300 ppm **or** INIT_FREQ_VARIATION < -300 ppm **or** DELTA_FREQ_200ns >1400 ppm **or** DELTA_FREQ_1000ns > 2200 ppm then **Fail**
10. Repeat the test for all remaining USB4 lanes.

3.3.8 Gen2 Rise/Fall Time Measurement

3.3.8.1 Reference

RISE_FALL_TIME - USB4 Specification Table 3-3.

3.3.8.2 Requirement

Rise Time and Fall Time ≥10ps.

3.3.8.3 Test Objective

Confirm that the rise time and fall time on the USB4 differential signals falls within the limits of the USB4 Specification.

3.3.8.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output alternating square pattern of 64 0’s and 64 1’s (SQ128) on all lanes with SSC turned on. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 based on local clock without SSC modulation on all lanes.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Evaluate 4Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 4Mpts. The evaluated record length shall be 50µs per channel:
    - Scope BW shall be 21GHz.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
5. T_{RISE} and T_{FALL} measurement thresholds are 20% to 80% of the differential swing voltage.
6. Capture the minimum of T_{RISE} and T_{FALL}.
7. If T_{RISE} < 10ps **or** T_{FALL}< 10ps then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.9 Gen2 Electrical Idle Voltage Measurement

3.3.9.1 Reference

V_ELEC_IDLE - USB4 Specification Table 3-3.

3.3.9.2 Requirement

V_ELEC_IDLE ≤ 20mV.

3.3.9.3 Test Objective

Confirm that the TX peak voltage during transmit electrical idle do not exceed the limits of the USB4 Specification.

3.3.9.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT to be in electrical idle mode.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the differential waveform V_{TX-P} - V_{TX-N} from the scope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 10Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 10Mpts. The evaluated record length shall be 125µs per channel.
   - No CDR, no average and no interpolation to be used.
   - Adjust vertical scale to fit signal into scope screen.
   - Scope BW shall be as specified in 6.1.
   - V_ELEC_IDLE shall be extracted after applying first order low-pass filter with 3 dB point at 1.25 GHz.
5. Measure $V_{peak-peak}$ (V_{TX-P} - V_{TX-N}).
6. If $V_{peak-peak}$ (V_{TX-P} - V_{TX-N}) > 20mV then **Fail.**
7. Repeat the test for all remaining USB4 lanes.

3.3.10 Gen2 Total Jitter Measurement

3.3.10.1 Reference

TJ - USB4 Specification Table 3-6.

3.3.10.2 Requirement

Total Jitter ≤ 0.38UIp-p.

3.3.10.3 Test Objective

Confirm that the transmitter Total Jitter falls within the limits of the USB4 Specification.

3.3.10.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. TJ is defined as the sum of all "deterministic" components plus 14.7 times the RJ RMS. 14.7 is the factor that accommodates a Bit Error Ratio value of 1E-13.
6. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
7. Capture TJ and DJ result.
8. If TJ > 0.38UIp-p.
    i. Configure DUT transmitter to output alternating square pattern of 1 0’s and 1 1’s (SQ2) on all lanes with SSC turned on. Use the same setup as above (pattern SQ2 instead of PRBS15).
    ii. Capture RJ result.
    iii. Calculate TJ to be TJ = DJ + 14.7*RJ (DJ from #7; based on PRBS15 pattern and RJ from #8.2; based on SQ2 pattern).

9. If TJ > 0.38UIp-p then **Fail.**
10. Repeat the test for all remaining USB4 lanes.

3.3.11 Gen2 UJ Measurement

3.3.11.1 Reference

UJ - USB4 Specification Table 3-6.

3.3.11.2 Requirement

Sum of uncorrelated DJ and RJ components ≤ 0.31UIp-p.

3.3.11.3 Test Objective

Confirm that the transmitter Sum of uncorrelated DJ and RJ components falls within the limits of the USB4 Specification.

3.3.11.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
6. Capture TJ and DDJ results. DDJ term definition is detailed in Appendix 9.2.
7. Calculate UJ=TJ-DDJ.
8. If UJ > 0.31UIp-p then **Fail.**
9. Repeat the test for all remaining USB4 lanes.

3.3.12 Gen2 UDJ Measurement

3.3.12.1 Reference

UDJ - USB4 Specification Table 3-6.

3.3.12.2 Requirement

Deterministic jitter that is uncorrelated to the transmitted data ≤ 0.17UIp-p.

3.3.12.3 Test Objective

Confirm that the transmitter Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.3.12.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture UDJ result.
7. If UDJ > 0.17UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.13 Gen2 DDJ Measurement

3.3.13.1 Reference

DDJ - USB4 Specification Table 3-6.

3.3.13.2 Requirement

Data Dependent jitter ≤ 0.15UIp-p.

3.3.13.3 Test Objective

Confirm that the transmitter Data Dependent jitter falls within the limits of the USB4 Specification.

3.3.13.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture DDJ result. DDJ term definition is detailed in Appendix 9.2.
7. If DDJ > 0.15UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.14 Gen2 Low Frequency UDJ Measurement

3.3.14.1 Reference

UDJ_LF - USB4 Specification Table 3-6.

3.3.14.2 Requirement

UDJ_LF ≤ 0.04UIp-p.

3.3.14.3 Test Objective

Confirm that the transmitter low frequency Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.3.14.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; after that apply 2nd order Low-Pass-Filter (LPF) defined by $H(s) = \frac{2\zeta\omega_n s + \omega_n^2}{s^2 + 2\zeta\omega_n s + \omega_n^2}$ Where ζ =3.5; ω_n=4.4e5 ; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500μs per channel.
   - Pattern length -> Periodic.
   - Jitter separation method shall be suitable for cross talk on signal.
   - Adjust vertical scale to fit signal into scope screen.
6. Capture UDJ_LF result.
7. If UDJ_LF > 0.04UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.15 Gen2 DCD Measurement

3.3.15.1 Reference

DCD - USB4 Specification Table 3-6.

3.3.15.2 Requirement

DCD ≤ 0.03UIp-p.

3.3.15.3 Test Objective

Confirm that the Even-odd jitter associated with Duty-Cycle-Distortion falls within the limits of the USB4 Specification.

3.3.15.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture DCD (Even-Odd jitter) result. DCD term definition is detailed in Appendix 9.3.
7. If DCD > 0.03UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.3.16 Gen2 AC Common Mode Measurement

3.3.16.1 Reference

AC_CM - USB4 Specification Table 3-6.

3.3.16.2 Requirement

AC_CM ≤ 100mVp-p.

3.3.16.3 Test Objective

Confirm that the transmitter common mode on the USB4 differential signals falls within the limits of the USB4 Specification.

3.3.16.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - Adjust vertical scale to fit signal into scope screen.
    - No CDR, no average and no interpolation to be used.
    - Scope BW shall be as specified in 6.1.
5. $V_{AC\text{-}CM}=(V_{TX\text{-}P}+V_{TX\text{-}N})/2$
6. If $V_{AC\text{-}CM}$ > 100mVp-p then **Fail**.
7. Repeat the test for all remaining USB4 lanes.

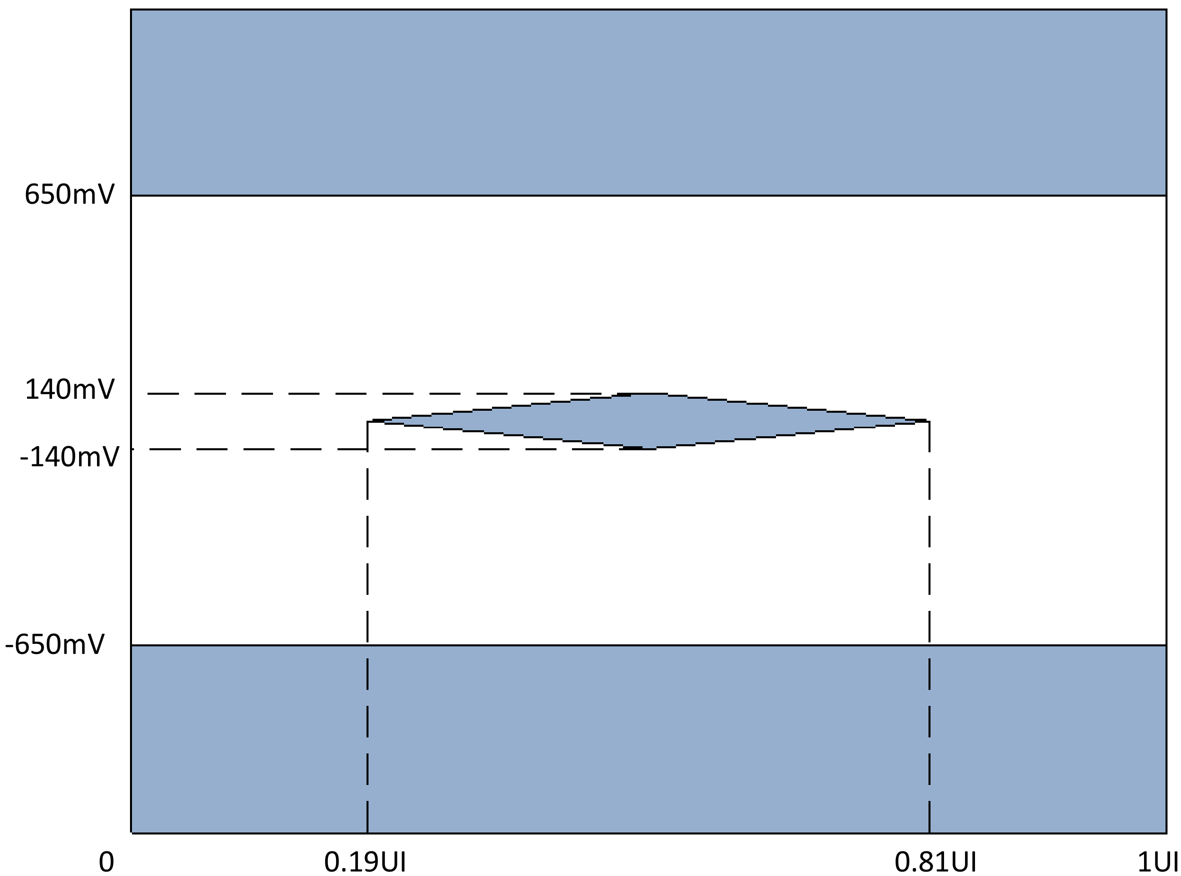
3.3.17 Gen2 Eye Diagram Measurement

3.3.17.1 Reference

Eye Diagram Mask – USB4 Specification Table 3-6.

USB4 Specification, Figure 3-15 – TX Mask Notations.

3.3.17.2 Requirement



3.3.17.3 Test Objective

Confirm that the differential signal on each USB4 differential lane has an eye opening that exceeds the limits on the eye opening in the USB4 Specification.

3.3.17.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.

4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.
6. Compare the data eye to the eye diagram mask. If any part of the waveform hits the mask, then **FAIL.**
7. Repeat the test for all remaining USB4 lanes.

3.3.18 Gen2 Total Jitter TP3 Measurement

3.3.18.1 Reference

TJ - USB4 Specification Table 3-7.

3.3.18.2 Requirement

Total Jitter ≤ 0.60UIp-p.

3.3.18.3 Test Objective

Confirm that the transmitter Total Jitter falls within the limits of the USB4 Specification.

3.3.18.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 2m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. TJ is defined as the sum of all "deterministic" components plus 14.7 times the RJ RMS. 14.7 is the factor that accommodates a Bit Error Ratio value of 1E-13.
8. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
9. Capture TJ and DJ result.

10. If TJ > 0.60UIp-p:

    i. Configure DUT transmitter to output alternating square pattern of 1 0's and 1 1's (SQ2) on all lanes with SSC turned on. Use the same setup as above (pattern SQ2 instead of PRBS15).

    ii. Capture RJ result.

    iii. Calculate TJ to be TJ = DJ + 14.7*RJ (DJ from #9; based on PRBS15 pattern and RJ from #10.2; based on SQ2 pattern).

11. If TJ > 0.60UIp-p then **Fail.**

12. Repeat the test for all remaining USB4 lanes.

3.3.19 Gen2 UJ TP3 Measurement

3.3.19.1 Reference

UJ – USB4 Specification Table 3-7.

3.3.19.2 Requirement

Sum of uncorrelated DJ and RJ components ≤ 0.31UIp-p.

3.3.19.3 Test Objective

Confirm that the transmitter Sum of uncorrelated DJ and RJ components falls within the limits of the USB4 Specification.

3.3.19.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 2m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
8. Capture TJ and DDJ results. DDJ term definition is detailed in Appendix 9.2.
9. Calculate UJ=TJ-DDJ.
10. If UJ > 0.31UIp-p then **Fail.**
11. Repeat the test for all remaining USB4 lanes.

3.3.20 Gen2 UDJ TP3 Measurement

3.3.20.1 Reference

UDJ - USB4 Specification Table 3-7.

3.3.20.2 Requirement

Deterministic jitter that is uncorrelated to the transmitted data ≤ 0.17UIp-p.

3.3.20.3 Test Objective

Confirm that the transmitter Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.3.20.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 2m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
8. Capture UDJ result.
9. If UDJ > 0.17UIp-p then **Fail.**
10. Repeat the test for all remaining USB4 lanes.

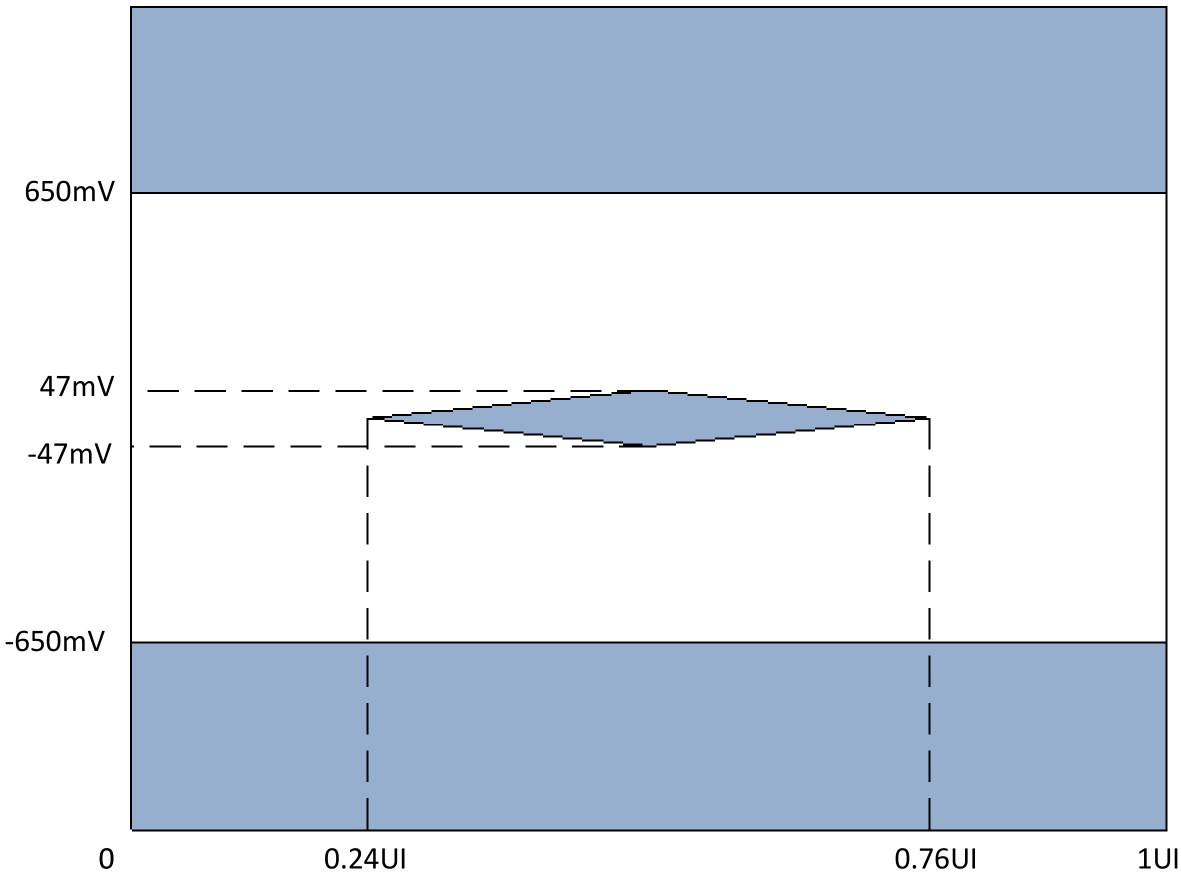
3.3.21 Gen2 Eye Diagram TP3 Measurement

3.3.21.1 Reference

Eye Diagram Mask – USB4 Specification Table 3-7.

USB4 Specification, Figure 3-15 – TX Mask Notations.

3.3.21.2 Requirement



3.3.21.3 Test Objective

Confirm that the differential signal on each USB4 differential lane has an eye opening that exceeds the limits on the eye opening in the USB4 Specification.

3.3.21.4 Test Method

1. Choose a supported USB4 Gen2 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The USB Type-C 2m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.

5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B.
6. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.
8. Compare the data eye to the eye diagram mask, if any part of the waveform hits the mask then **Fail.**
9. Repeat the test for all remaining USB4 lanes.

3.4 Gen3 Router Assembly Transmitter Compliance

Note: *Refer to Sections 3 - 3.2 for test Setup.*

3.4.1 Gen3 Transmitter Equalization

3.4.1.1 Reference

USB4 Specification Table 3-3 and 3-5.

3.4.1.2 Requirement

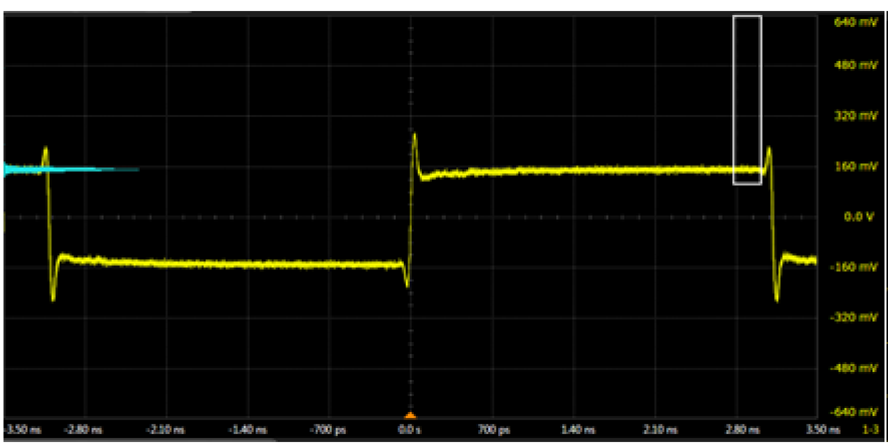
Transmitter swing 3.5 ± 1dB (for preset 15 only)
Pre-shoot, de-emphasis ± 1dB for the presets in the Transmit Equalization Preset Table above.

3.4.1.3 Test Objective

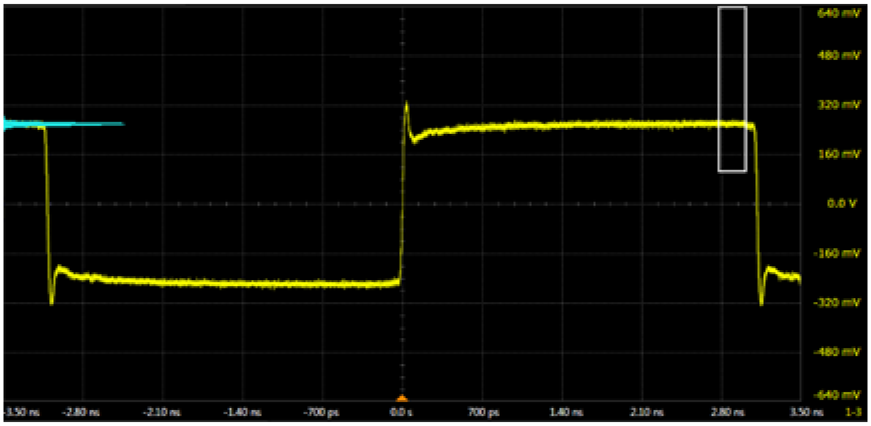
Confirm that the transmitter equalization falls within the limits of the USB4 Specification.

3.4.1.4 Test Method

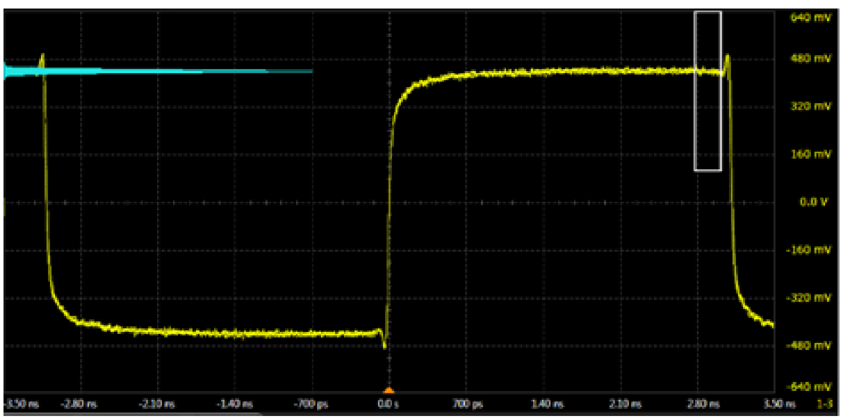
1. Set preset 0.
2. Choose a supported USB4 Gen3 speed.
3. Scope BW shall be as specified in 6.1.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Configure DUT transmitter to output alternating square pattern of 64 0’s and 64 1’s on all lanes with SSC turned on with pre-shoot and with de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with pre-shoot and with de-emphasis based on local clock without SSC modulation on all lanes.
6. Adjust vertical scale to fit signal into scope screen.
7. Use average of 150 cycles; no CDR and no interpolation to be used.



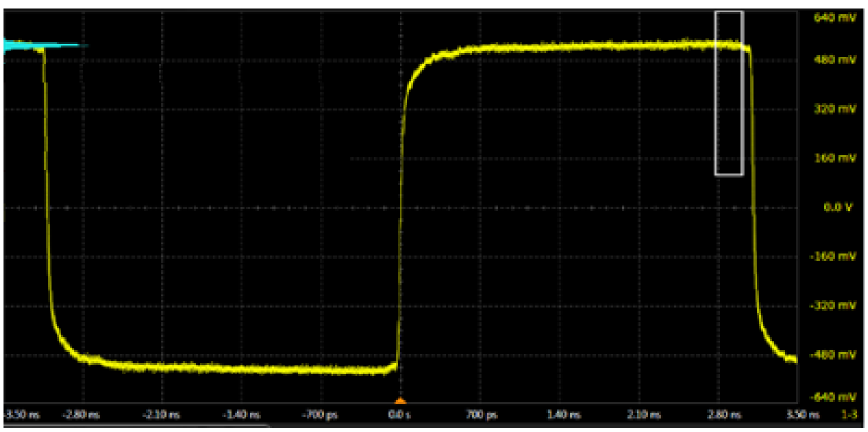
8. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_1.
9. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with no pre-shoot and with de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with de-emphasis based on local clock without SSC modulation on all lanes.
10. Adjust vertical scale to fit signal into scope screen.
11. Use average of 150 cycles; no CDR and no interpolation to be used.



12. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_2.
13. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with pre-shoot but with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.
14. Adjust vertical scale to fit signal into scope screen.
15. Use average of 150 cycles; no CDR and no interpolation to be used.

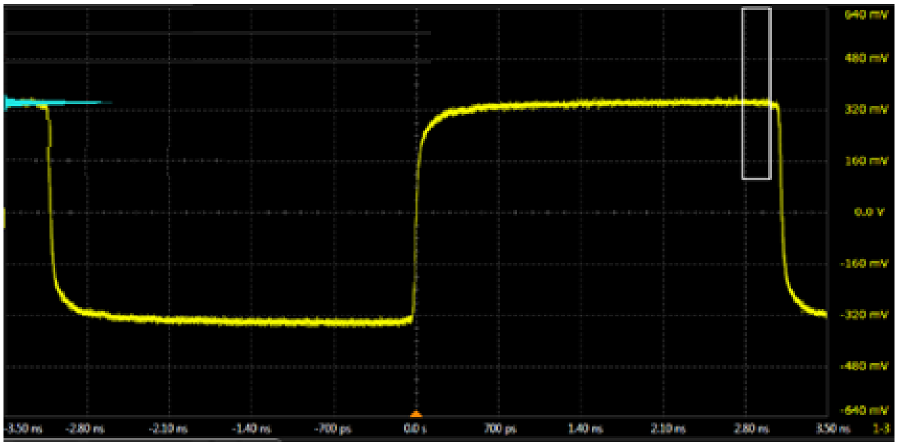


16. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_3.
17. Set Pre-shoot to be $20 * log_{10} {}^{V_2}/_{V_1}$.
18. Set De-emphasis to be $20 * log_{10} {}^{V_1}/_{V_3}$.
19. If Pre-shoot is not within ± 1dB of the matching value in the Transmit Equalization Preset Table, then **Fail.**
20. If De-emphasis is not within ± 1dB of the matching value in the Transmit Equalization Preset Table, then **Fail.**
21. Repeat for all Presets in the Transmit Equalization Preset Table.
22. Set the DUT to preset 0.
23. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with no pre-shoot and with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.
24. Adjust vertical scale to fit signal into scope screen.
25. Use average of 150 cycles; no CDR and no interpolation to be used.



26. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) ,average them and mark it as V_0.
27. Set the DUT to preset 15.
28. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's on all lanes with SSC turned on with no pre-shoot and with no de-emphasis. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 with no pre-shoot and with no de-emphasis based on local clock without SSC modulation on all lanes.
29. Adjust vertical scale to fit signal into scope screen.

30. Use average of 150 cycles; no CDR and no interpolation to be used.



31. Measure differential amplitude voltage of bits 57-62 ($|V_{bits(57-62)}(64\ bits\ of\ 1's) - V_{bits(57-62)}(64\ bits\ of\ 0's)|$) and mark it as V_{15}.

32. Set Swing to be $20 * log_{10} {V_0}/{V_{15}}$

33. If Swing < 2.5 dB or Swing > 4.5 dB then **Fail.**

34. Repeat the test for all remaining USB4 lanes.

3.4.2 Gen3 Minimum Unit Interval Measurement

3.4.2.1 Reference

UI USB4 Specification Table 3-8.

3.4.2.2 Requirement

49.985ps ≤ Minimum Unit Interval ≤ 50.015ps.

3.4.2.3 Test Objective

Confirm that the Minimum Unit Interval under all conditions does not exceed minimum or maximum limits of the USB4 Specification.

3.4.2.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. Minimum UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 6000 symbols.
6. Capture 1 measurements and calculate both Min and Max of Minimum UI values.
7. If Max Minimum UI > 50.015ps **or** Min Minimum UI < 49.985ps then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.4.3 Gen3 SSC Down Spread Range Measurement

3.4.3.1 Reference

SSC_Down_Spread_Range - USB4 Specification Table 3-3.

3.4.3.2 Requirement

0.4% ≤ SSC_Down_Spread_Range ≤ 0.5%.

3.4.3.3 Test Objective

Confirm that the Dynamic range of SSC down-spreading falls within the limits of the USB4 Specification.

3.4.3.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 6000 symbols.
6. Capture values of Max(maximum UI), Min(maximum UI), Max(minimum UI) and Min(minimum UI). calculate the Max and Min value of SSC_Down_Spread_Range:
    - Max(Max Deviation)= 100*((Bit_Rate - (1/ Max(maximum UI)))/ Bit_Rate)
    - Min(Min Deviation)= 100*((Bit_Rate - (1/ Min(minimum UI)))/ Bit_Rate)
    - Min(Max Deviation)= 100*((Bit_Rate - (1/ Min(maximum UI)))/ Bit_Rate)
    - Max(Min Deviation)= 100*((Bit_Rate - (1/ Max(minimum UI)))/ Bit_Rate)
    - Bit_Rate is the base frequency with 0ppm
    - Max SSC_Down_Spread_Range = Max(Max Deviation) – Min(Min Deviation)
    - Min SSC_Down_Spread_Range = Min(Max Deviation) – Max(Min Deviation)
7. If Max SSC_Down_Spread_Range > 0.5% **or** Min SSC_Down_Spread_Range < 0.4% then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.4.4 Gen3 SSC Down Spread Rate Measurement

3.4.4.1 Reference

SSC_Down_Spread_Rate - USB4 Specification Table 3-3.

3.4.4.2 Requirement

30KHz ≤ SSC_Down_Spread_Rate ≤ 33KHz.

3.4.4.3 Test Objective

Confirm that the SSC down-spreading modulation rate during steady-state falls within the limits of the USB4 Specification.

3.4.4.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. UI shall be calculated dynamically using a uniform moving average filter procedure with window size of 6000 symbols.
6. Capture values of Max SSC_Down_Spread_Rate and Min SSC_Down_Spread_Rate.
7. If 30KHz > Min SSC_Down_Spread_Rate **or** Max SSC_Down_Spread_Rate > 33KHz then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.4.5 Gen3 SSC Phase Deviation Measurement

3.4.5.1 Reference

SSC_Phase_Deviation - USB4 Specification Table 3-3.

3.4.5.2 Requirement

2.5ns p-p ≤ SSC_Phase_Deviation ≤ 22ns p-p.

3.4.5.3 Test Objective

Confirm that the SSC Phase Deviation falls within the limits of the USB4 Specification during steady-state.

3.4.5.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and post process it with an appropriate software:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The SSC phase deviation shall be extracted from the transmitted signal.
    - The SSC phase deviation shall be extracted from the signal phase after applying a 2^{nd} order Low-Pass-Filter (LPF) with 3dB point at 5MHz.
    - Scope BW shall be as specified in 6.1.
5. If SSC_Phase_Deviation < 2.5ns p-p or SSC_Phase_Deviation > 22ns p-p then **Fail**.
6. Repeat the test for all remaining USB4 lanes.

3.4.6 Gen3 SSC Slew Rate Measurement

3.4.6.1 Reference

SSC_Slew_Rate - USB4 Specification Table 3-3.

3.4.6.2 Requirement

SSC_Slew_Rate ≤ 1250 ppm/µs.

3.4.6.3 Test Objective

Confirm that the SSC Slew Rate during steady-state falls within the limits of the specification.

3.4.6.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and post process it with an appropriate software:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The SSC slew rate shall be extracted from the transmitted signal over measurement intervals of 0.5µs.
    - The SSC slew rate shall be extracted from the signal phase after applying a 2nd order Low-Pass-Filter (LPF) with 3dB point at 5MHz.
    - Scope BW shall be as specified in 6.1.
    - SSC_SLEW_RATE measurement shall be done in steady-state after the Link training phase is completed.
5. If SSC_Slew_Rate > 1250 ppm/µs then **Fail.**
6. Repeat the test for all remaining USB4 lanes.

3.4.7 Gen3 TX Frequency Variation Training Measurement

3.4.7.1 Reference

TX_FREQ_VARIATIONS_TRAINING – USB4 Specification Table 3-3.

3.4.7.2 Requirement

TX_FREQ_VARIATIONS_TRAINING:
-300 ≤ INIT_FREQ_VARIATION ≤ 300 ppm
DELTA_FREQ_200ns ≤ 1400 ppm
DELTA_FREQ_1000ns ≤ 2200 ppm

3.4.7.3 Test Objective

Confirm that the Frequency variation during Link training falls within the limits of the specification.

***Note**: This test shall be applied only when the DUT contains Re-Timer.*

3.4.7.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. For emulating link training, DUT shall be configured such that the Router transmitting PRBS31 with SSC modulation on all lanes.
3. The Re-Timer placed near the USB Type-C connector shall be configured to transmit SQ128 based on local clock without SSC modulation on all lanes. If additional Re-Timer exist in the DUT, it shall be configured to transmit SQ4 based on local clock without SSC modulation on all lanes.
4. Initiate clock switch from local clock to recovered clock for the Re-Timer placed near the USB Type-C connector.
5. The cables from the plug test fixture to the scope shall be de-embedded.
6. Capture the clock switch period during three stages: pre-clock switch, clock switch and post-clock switch in a single waveform and post process it as following:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - The frequency variation shall be performed over the transmitted signal.
    - The signal phase shall be extracted after applying a 2nd order Low-Pass-Filter (LPF) with 3dB point at 5MHz.
    - Calculate the initial non-modulated transmit frequency aka INIT_FREQ_VARIATION.

- Calculate the frequency variation during Link training (switch clock) over 200ns measurement windows aka DELTA_FREQ_200ns.
- Calculate the frequency variation during Link training (switch clock) over 1000ns measurement windows aka DELTA_FREQ_1000ns.
- Scope BW shall be as specified in 6.1.

7. If additional Re-Timer exist in the DUT, then the 2nd clock switch need to be analyzed as well from the saved waveform in step 6 above.
8. Repeat this procedure 20 times and report the worst captured waveform.
9. If INIT_FREQ_VARIATION > 300 ppm **or** INIT_FREQ_VARIATION < -300 ppm **or** DELTA_FREQ_200ns >1400 ppm **or** DELTA_FREQ_1000ns > 2200 ppm then **Fail.**
10. Repeat the test for all remaining USB4 lanes.

3.4.8 Gen3 Rise/Fall Time Measurement

3.4.8.1 Reference

RISE_FALL_TIME - USB4 Specification Table 3-3.

3.4.8.2 Requirement

Rise Time and Fall Time ≥10ps.

3.4.8.3 Test Objective

Confirm that the rise times and fall times on the USB4 differential signals falls within the limits of the USB4 Specification.

3.4.8.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output alternating square pattern of 64 0's and 64 1's (SQ128) on all lanes with SSC turned on. If a Re-Timer placed near the USB Type-C connector, then the Re-Timer transmitter shall be configured to transmit SQ128 based on local clock without SSC modulation on all lanes.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Evaluate 4Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 4Mpts. The evaluated record length shall be 50µs per channel:
    - Scope BW shall be 21GHz.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
5. T_{RISE} and T_{FALL} measurement thresholds are 20% to 80% of the differential swing voltage.
6. Capture the minimum of T_{RISE} and T_{FALL}.
7. If T_{RISE} < 10ps **or** T_{FALL}< 10ps then **Fail**.
8. Repeat the test for all remaining USB4 lanes.

3.4.9 Gen3 Electrical Idle Voltage Measurement

3.4.9.1 Reference

V_ELEC_IDLE - USB4 Specification Table 3-3.

3.4.9.2 Requirement

V_ELEC_IDLE ≤ 20mV.

3.4.9.3 Test Objective

Confirm that the TX peak voltage during transmit electrical idle do not exceed the limits of the USB4 Specification.

3.4.9.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT to be in electrical idle mode.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the differential waveform $V_{TX\text{-}P}$ - $V_{TX\text{-}N}$ from the scope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 10Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 10Mpts. The evaluated record length shall be 125µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
    - V_ELEC_IDLE shall be extracted after applying first order low-pass filter with 3 dB point at 1.25 GHz.
5. Measure $V_{peak\text{-}peak}$ ($V_{TX\text{-}P}$ - $V_{TX\text{-}N}$).
6. If $V_{peak\text{-}peak}$ ($V_{TX\text{-}P}$ - $V_{TX\text{-}N}$) > 20mV then **Fail**.
7. Repeat the test for all remaining USB4 lanes.

3.4.10 Gen3 Total Jitter Measurement

3.4.10.1 Reference

TJ - USB4 Specification Table 3-8.

3.4.10.2 Requirement

Total Jitter ≤ 0.46UIp-p.

3.4.10.3 Test Objective

Confirm that the transmitter Total Jitter falls within the limits of the USB4 Specification.

3.4.10.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. TJ is defined as the sum of all "deterministic" components plus 14.7 times the RJ RMS. 14.7 is the factor that accommodates a Bit Error Ratio value of 1E-13.
6. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
7. Capture TJ and DJ result.
8. If TJ > 0.46UIp-p:
    i. Configure DUT transmitter to output alternating square pattern of 1 0's and 1 1's (SQ2) on all lanes with SSC turned on. Use the same setup as above (pattern SQ2 instead of PRBS15).

ii. Capture RJ result.

iii. Calculate TJ to be TJ = DJ + 14.7*RJ (DJ from #7; based on PRBS15 pattern and RJ from #8.2;based on SQ2 pattern).

9. If TJ > 0.46UIp-p then **Fail.**

10. Repeat the test for all remaining USB4 lanes.

3.4.11 Gen3 UJ Measurement

3.4.11.1 Reference

UJ - USB4 Specification Table 3-8.

3.4.11.2 Requirement

Sum of uncorrelated DJ and RJ components ≤ 0.31UIp-p.

3.4.11.3 Test Objective

Confirm that the transmitter Sum of uncorrelated DJ and RJ components falls within the limits of the USB4 Specification.

3.4.11.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
6. Capture TJ and DDJ results. DDJ term definition is detailed in Appendix 9.2.
7. Calculate UJ=TJ-DDJ.
8. If UJ > 0.31UIp-p then **Fail**.
9. Repeat the test for all remaining USB4 lanes.

3.4.12 Gen3 UDJ Measurement

3.4.12.1 Reference

UDJ - USB4 Specification Table 3-8.

3.4.12.2 Requirement

Deterministic jitter that is uncorrelated to the transmitted data ≤ 0.17UIp-p.

3.4.12.3 Test Objective

Confirm that the transmitter Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.4.12.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture UDJ result.
7. If UDJ > 0.17UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.4.13 Gen3 DDJ Measurement

3.4.13.1 Reference

DDJ - USB4 Specification Table 3-8.

3.4.13.2 Requirement

Data Dependent jitter ≤ 0.21UIp-p.

3.4.13.3 Test Objective

Confirm that the transmitter Data Dependent jitter falls within the limits of the USB4 Specification.

3.4.13.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture DDJ result. DDJ term definition is detailed in Appendix 9.2.
7. If DDJ > 0.21UIp-p then **Fail.**
8. Repeat the test for all remaining USB4 lanes.

3.4.14 Gen3 Low Frequency UDJ Measurement

3.4.14.1 Reference

UDJ_LF - USB4 Specification Tables 3-8.

3.4.14.2 Requirement

UDJ_LF ≤ 0.07UIp-p.

3.4.14.3 Test Objective

Confirm that the transmitter low frequency Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.4.14.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; after that apply 2nd order Low-Pass-Filter (LPF) defined by $H(s) = \frac{2\zeta\omega_n s+\omega_n{}^2}{s^2+2\zeta\omega_n s+\omega_n{}^2}$ Where ζ =3.5; ω_n=4.4e5 ; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
   - Pattern length -> Periodic.
   - Jitter separation method shall be suitable for cross talk on signal.
   - Adjust vertical scale to fit signal into scope screen.
6. Capture UDJ_LF result.
7. If UDJ_LF > 0.07UIp-p then **Fail**.
8. Repeat the test for all remaining USB4 lanes.

3.4.15 Gen3 DCD Measurement

3.4.15.1 Reference

DCD - USB4 Specification Tables 3-8.

3.4.15.2 Requirement

DCD ≤ 0.03UIp-p.

3.4.15.3 Test Objective

Confirm that the Even-odd jitter associated with Duty-Cycle-Distortion falls within the limits of the USB4 Specification.

3.4.15.4 Test Method

1. Choose a supported USB4 Gen3.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
6. Capture DCD (Even-Odd jitter) result. DCD term definition is detailed in Appendix 9.3.
7. If DCD > 0.03UIp-p then **Fail**.
8. Repeat the test for all remaining USB4 lanes.

3.4.16 Gen3 AC Common Mode Measurement

3.4.16.1 Reference

AC_CM - USB4 Specification Table 3-8.

3.4.16.2 Requirement

AC_CM ≤ 100mVp-p.

3.4.16.3 Test Objective

Confirm that the transmitter common mode on the USB4 differential signals falls within the limits of the USB4 Specification.

3.4.16.4 Test Method

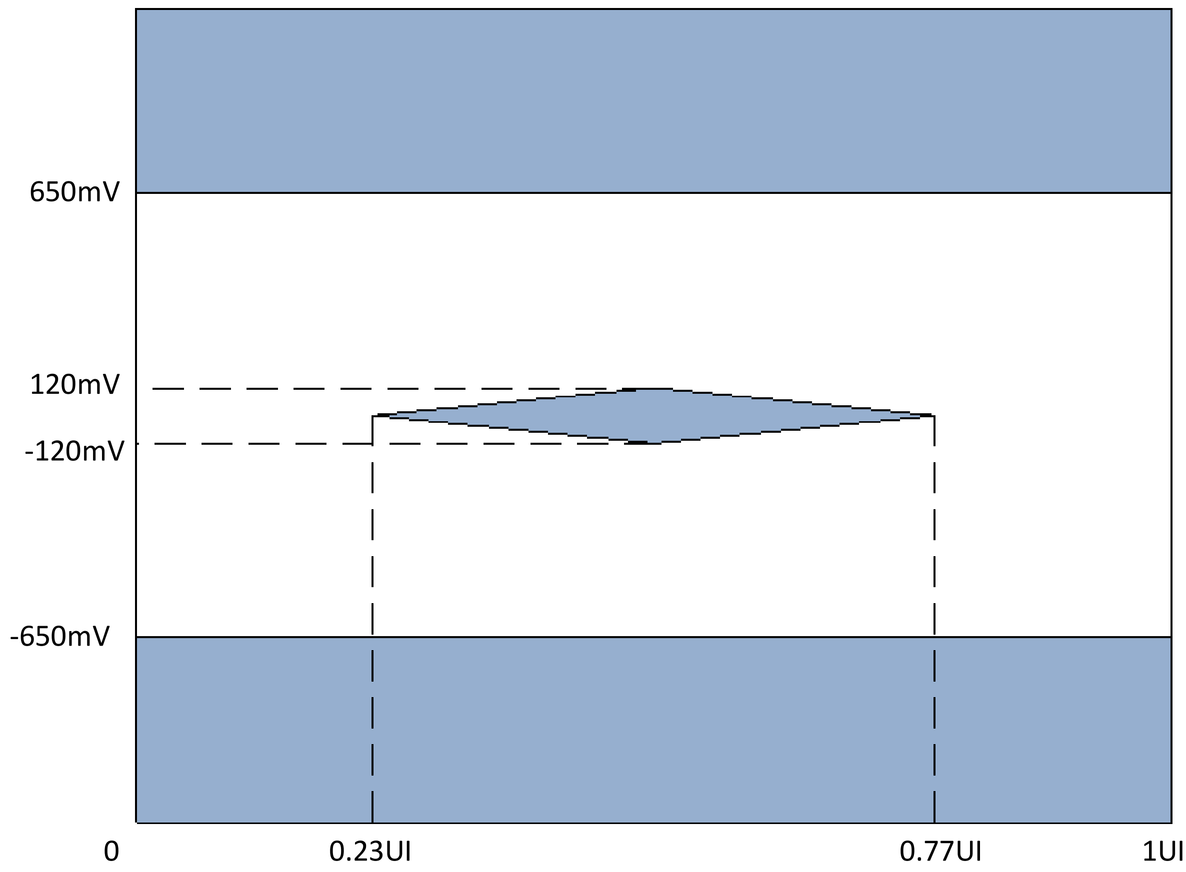
1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - Adjust vertical scale to fit signal into scope screen.
    - No CDR, no average and no interpolation to be used.
    - Scope BW shall be as specified in 6.1.
5. $V_{AC\text{-}CM}=(V_{TX\text{-}P}+V_{TX\text{-}N})/2$
6. If $V_{AC\text{-}CM}$ > 100mVp-p then **Fail**.
7. Repeat the test for all remaining USB4 lanes.

3.4.17 Gen3 Eye Diagram Measurement

3.4.17.1 References

Eye Diagram Mask – USB4 Specification Table 3-8.
USB4 Specification, Figure 3-15 – TX Mask Notations.

3.4.17.2 Requirement



3.4.17.3 Test Objective

Confirm that the differential signal on each USB4 differential lane has an eye opening that exceeds the limits on the eye opening in the USB4 Specification.

3.4.17.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining

effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.

5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.
6. Compare the data eye to the eye diagram mask. If any part of the waveform hits the mask, then **Fail**.
7. Repeat the test for all remaining USB4 lanes.

3.4.18 Gen3 Total Jitter TP3 Measurement

3.4.18.1 Reference

TJ - USB4 Specification Table 3-9.

3.4.18.2 Requirement

Total Jitter ≤ 0.60UIp-p.

3.4.18.3 Test Objective

Confirm that the transmitter Total Jitter falls within the limits of the USB4 Specification.

3.4.18.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 0.8m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. TJ is defined as the sum of all "deterministic” components plus 14.7 times the RJ RMS. 14.7 is the factor that accommodates a Bit Error Ratio value of 1E-13.
8. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
    - Removing intrinsic scope noise and jitter is recommended.
9. Capture TJ and DJ result.

10. If TJ > 0.60UIp-p:

    i. Configure DUT transmitter to output alternating square pattern of 1 0's and 1 1's (SQ2) on all lanes with SSC turned on. Use the same setup as above (pattern SQ2 instead of PRBS15).

    ii. Capture RJ result.

    iii. Calculate TJ to be TJ = DJ + 14.7*RJ (DJ from #9; based on PRBS15 pattern and RJ from #10.2; based on SQ2 pattern).

11. If TJ > 0.60UIp-p then **Fail**.

12. Repeat the test for all remaining USB4 lanes.

3.4.19 Gen3 UJ TP3 Measurement

3.4.19.1 Reference

UJ – USB4 Specification Table 3-9.

3.4.19.2 Requirement

Sum of uncorrelated DJ and RJ components ≤ 0.31UIp-p.

3.4.19.3 Test Objective

Confirm that the transmitter Sum of uncorrelated DJ and RJ components falls within the limits of the USB4 Specification.

3.4.19.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 0.8m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
   - Pattern length -> Periodic.
   - Jitter separation method shall be suitable for cross talk on signal
   - Adjust vertical scale to fit signal into scope screen.
   - Referenced to 1E-13 statistics.
   - Removing intrinsic scope noise and jitter is recommended.
8. Capture TJ and DDJ results. DDJ term definition is detailed in Appendix 9.2.
9. Calculate UJ=TJ-DDJ.
10. If UJ > 0.31UIp-p then **Fail**.
11. Repeat the test for all remaining USB4 lanes.

3.4.20 Gen3 UDJ TP3 Measurement

3.4.20.1 Reference

UDJ - USB4 Specification Table 3-9.

3.4.20.2 Requirement

Deterministic jitter that is uncorrelated to the transmitted data ≤ 0.17UIp-p.

3.4.20.3 Test Objective

Confirm that the transmitter Uncorrelated Deterministic Jitter falls within the limits of the USB4 Specification.

3.4.20.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS15 on all lanes with SSC turned on.
3. The USB Type-C 0.8m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B. If not reliable Jitter results with DFE then apply CTLE only.
6. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
8. Capture UDJ result.
9. If UDJ > 0.17UIp-p then **Fail**.
10. Repeat the test for all remaining USB4 lanes.

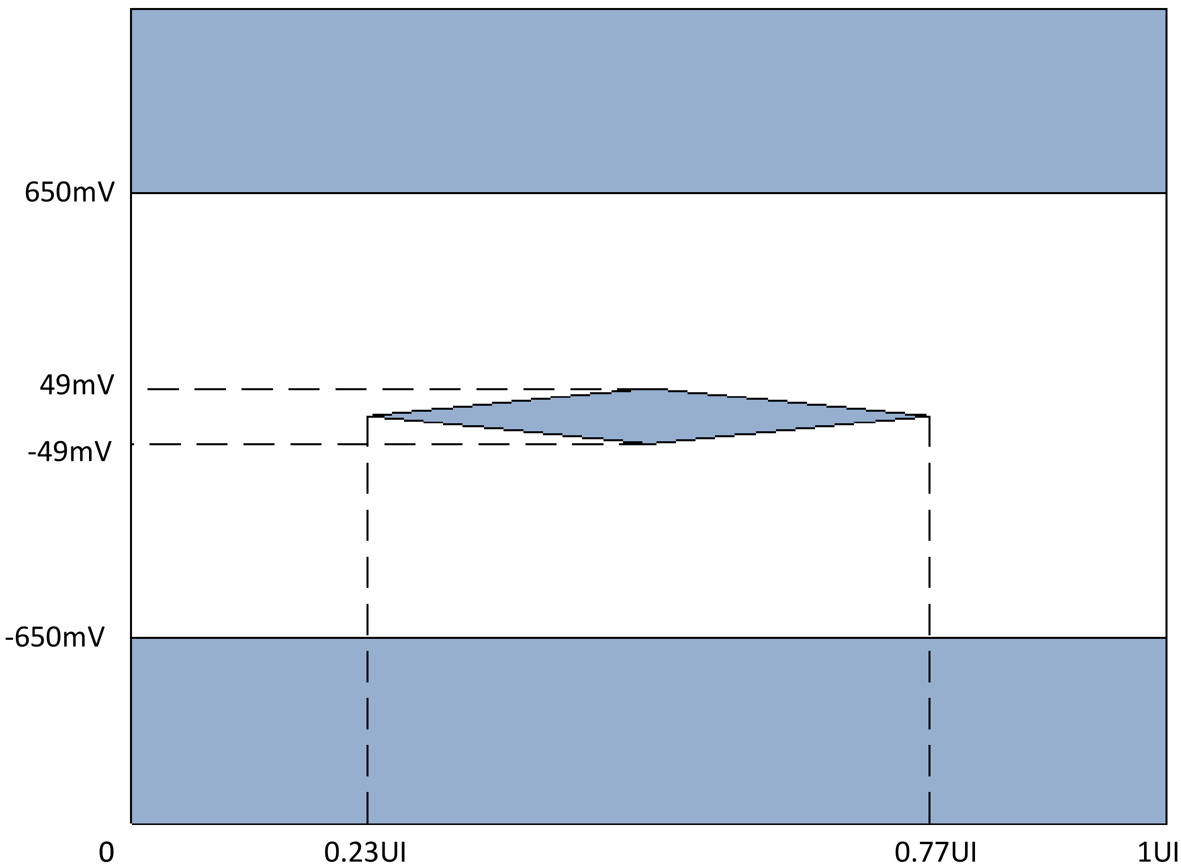
3.4.21 Gen3 Eye Diagram TP3 Measurement

3.4.21.1 Reference

Eye Diagram Mask – USB4 Specification Table 3-9.

USB4 Specification, Figure 3-15 – TX Mask Notations.

3.4.21.2 Requirement



3.4.21.3 Test Objective

Confirm that the differential signal on each USB4 differential lane has an eye opening that exceeds the limits on the eye opening in the USB4 Specification.

3.4.21.4 Test Method

1. Choose a supported USB4 Gen3 speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The USB Type-C 0.8m cable shall be embedded in the scope. Use the embedding file as defined in 6.2.
4. The cables from the plug test fixture to the scope shall be de-embedded.

5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B.
6. Measurement shall be done with a reference CDR modeled by a 2^{nd} order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.
8. Compare the data eye to the eye diagram mask. If any part of the waveform hits the mask, then **Fail**.
9. Repeat the test for all remaining USB4 lanes.

3.5 Transmitter Return Loss Gen2 and Gen3

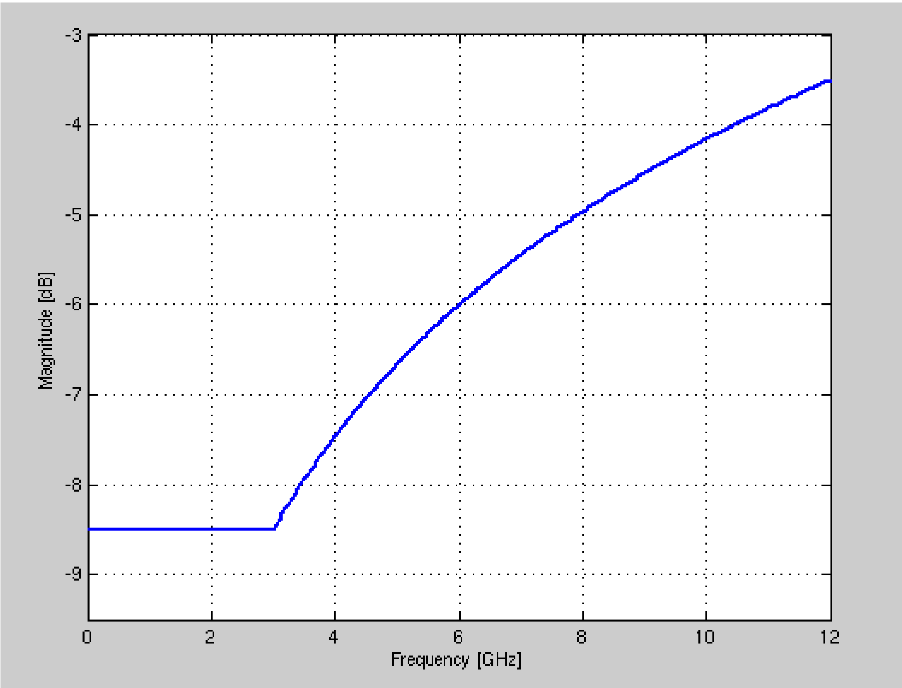
3.5.1 Transmitter Differential Return Loss

3.5.1.1 Reference

RL_DIFF - USB4 Specification Table 3-3.

3.5.1.2 Requirement

$$\mathrm{SDD11(f)} = \begin{cases} -8.5 & 0.05 < f_{GHz} \leq 3 \\ -3.5 + 8.3 \cdot \log 10\left(\frac{f_{GHz}}{12}\right) & 3 < f_{GHz} \leq 12 \end{cases}$$



3.5.1.3 Test Objective

Confirm that the Differential Return loss falls within the limits of the USB4 Specification.

3.5.1.4 Test Method

1. Choose a supported USB4 speed to start with.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. Setup the Network Analyzer with measurement:

   Note: *As the extra loss and distortion elements may be compensated by physical or mathematical means, use gating option to compensate for the parts of the fixture that are with 100ohm impedance. No de-embedding or gating to the mated USB Type-C connector or the TF traces:*

   - *Frequency range of 50MHz to 12GHz.*

- *IF BW of 1KHz.*
- *At least 1600 points.*
- *Impedance 85 ohm differential.*
- *Define the Topology to Bal.*
- *Define the measurement at SDD11.*

4. Calibrate the network analyzer and test cables using a 2-port auto calibration kit.
5. Connect Lane under test TX_P, TX_N to the Network Analyzer.

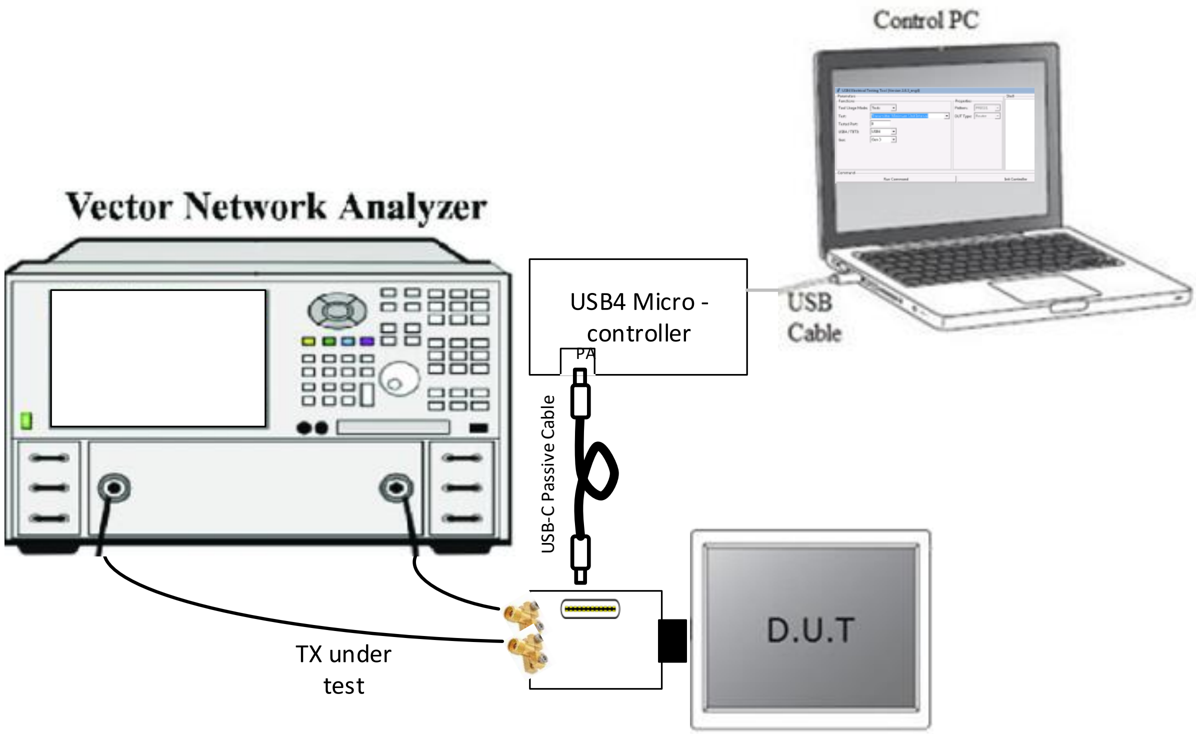


Figure 3. Transmitter Return Loss Test Setup

6. Measure the Differential R. Loss with the Network Analyzer.
7. If Differential Return loss violated the above requirement, then **Fail**.
8. Repeat the test for all remaining USB4 lanes.
9. Repeat the test for all supported USB4 Gen2 and Gen3 speeds.

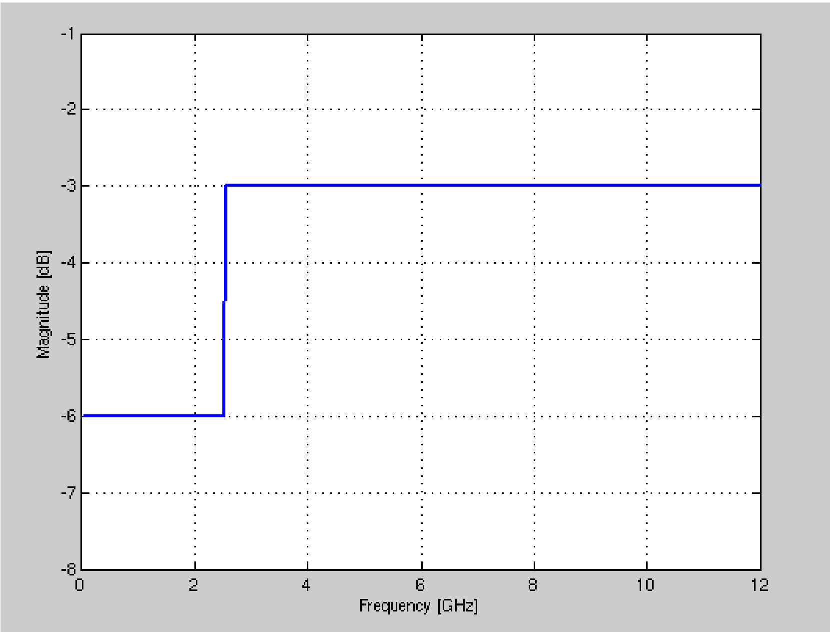
3.5.2 Transmitter Common Mode Return Loss

3.5.2.1 Reference

RL_COMM - USB4 Specification Table 3-3.

3.5.2.2 Requirement

$$\mathrm{SCC11(f)} = \begin{cases} -6 & 0.05 < f_{GHz} \leq 2.5 \\ -3 & 2.5 < f_{GHz} \leq 12 \end{cases}$$



3.5.2.3 Test Objective

Confirm that the Common Mode Return loss falls within the limits of the USB4 Specification.

3.5.2.4 Test Method

1. Choose a supported USB4 speed to start with.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. Setup the Network Analyzer with measurement:

   Note: *As the extra loss and distortion elements may be compensated by physical or mathematical means, use gating option to compensate for the parts of the fixture that are with 100ohm impedance. No de-embedding or gating to the mated USB Type-C connector or the TF traces:*

   - *Frequency range of 50MHz to 12GHz.*
   - *IF BW of 1KHz.*
   - *At least 1600 points.*

- *Impedance 85 ohm differential.*
- *Define the Topology to Bal.*
- *Define the measurement at SCC11.*

4. Calibrate the network analyzer and test cables using a 2-port auto calibration kit.
5. Connect Lane under test TX_P, TX_N to the Network Analyzer.
6. Measure the Common Mode R. Loss with the Network Analyzer.
7. If Common Mode Return loss violated the above requirement, then **Fail**.
8. Repeat the test for all remaining USB4 lanes.
9. Repeat the test for all supported USB4 Gen2 and Gen3 speeds.

4 Router Assembly Receiver Testing

This section describes the tests required to verify whether the USB4 Receiver is compliant with the USB4 Specification.

The following sections provide detailed information on the setup and testing of the USB4 parameters. In the event of a discrepancy, the USB4 Specification prevails.

- Router Assembly receiver compliance testing is defined at the output of a “golden” plug fixture at the TP3’ reference point and at the output of a “golden” receptacle fixture at the TP3 reference point.
- TP3’- Reference measurement point located at the plug side of the Router Assembly RX input.
- TP3- Reference measurement point located at USB Type-C receptacle output on the far-end side of passive cable. Used as a reference point for passive installations. All the measurements at this point shall be done while applying reference equalization.
- All measurements shall be referenced to the TP3’/TP3 compliance point.
- Calibration shall be applied in cases where direct measurement is not feasible.
- All jitter measurements is done on the average frequency.

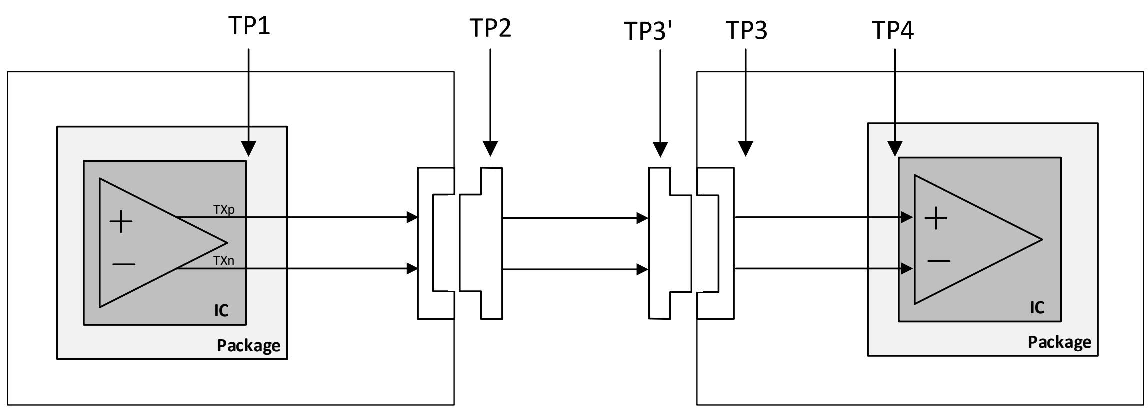


Figure 4. USB4 RX Compliance Points Definition

The ability of a Router Assembly input to tolerate the worst-case incoming signal is examined using a stressed receiver test. A Router Assembly receiver shall operate at BER of 1E-12 or lower without forward-error-correction nor pre-coding applied when a stressed signal is driven at its input. Tolerance testing shall be performed with down-spreading of the clock enabled and while all ports are active. There are two test setups that shall be used for evaluating the Router Assembly receiver tolerance:

“Case1”, which addresses installations with low Insertion-Loss.

“Case2”, which addresses installations with maximum Insertion-Loss.

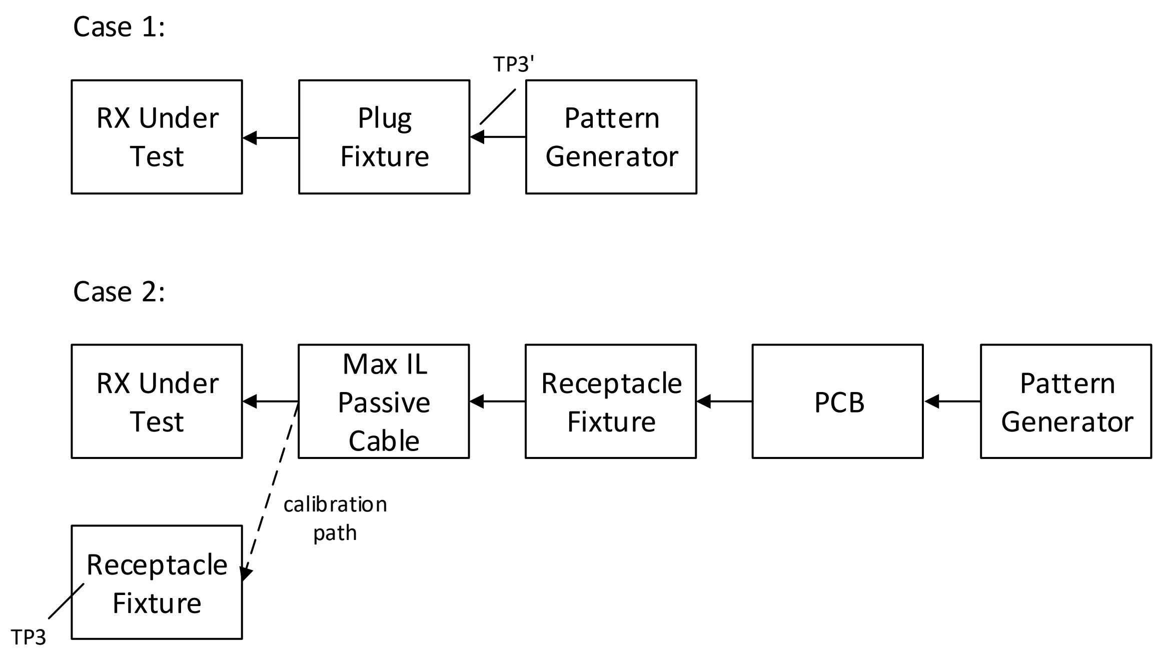


Figure 5. USB4 RX Tolerance Test Topologies

4.1 Receiver Test Setup

There are two physical setups required for Receiver testing, the Calibration Setup and the Test Setup.

Note: Before beginning any test or data acquisition, the Generator and Scope must be warmed, calibrated, and cables de-skewed.

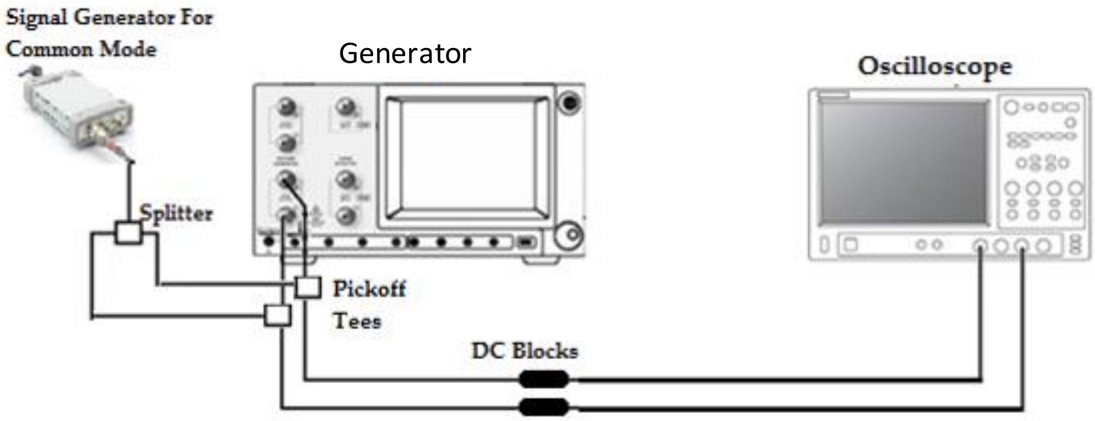


Figure 6. Receiver Calibration Setup – TP3'

4.2 Receiver Stressed Eye Calibration

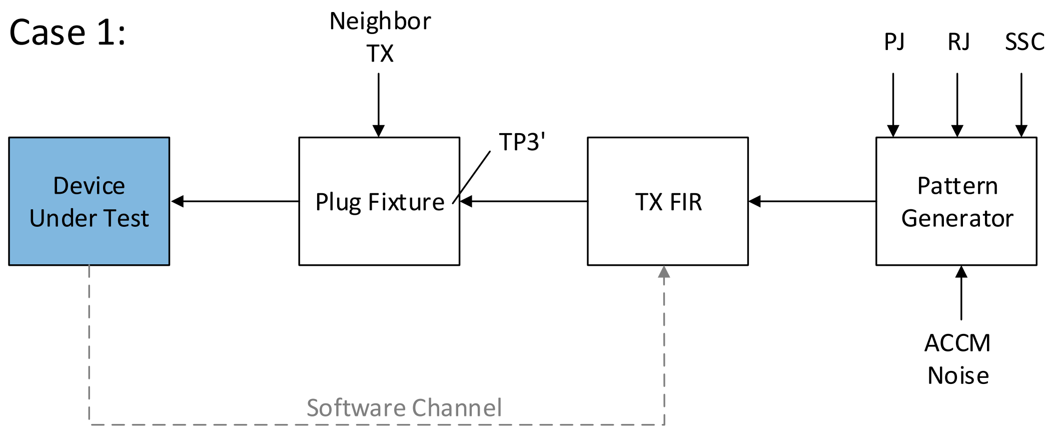
The USB4 Specification outlines a minimum Receiver Eye Diagram which is measured at the USB Type-C connector at the plug side.

The following procedure describes how to calibrate the USB4 minimum Receiver Eye Diagram.

Connect the test setup to the Scope as in Figure 6 above.

1. Connect the DATA+/DATA- Output of the Generator to Pickoff Tee.
2. Use splitter at the output of the signal generator and connect the splitter output to the Pickoff Tee. The pickoff Tee shall be connected directly to the Generator, without any RF cable.
3. Use phase matched cables to connect the Pickoff Tee sum out to DC blocks. Add the DC block over the Pickoff Tee out pin directly.
4. Connect pair of phase matched cables from the DC blocks to the oscilloscope (maximum 1m length).

4.2.1 Stressed Electrical Signal for TP3' (Case 1):



Set the SSC on the pattern generator to be:

- SSC with modulation wave shape triangle.
- Modulation frequency for both Gen2 and Gen3 is 32KHz.
- Spread deviation from +300ppm up to -5300ppm.

4.2.1.1 Data Dependent Jitter

4.2.1.1.1 Reference

USB4 Specification Table 3-5 (reproduced above).

4.2.1.1.2 Requirement

Data Dependent Jitter – Minimum possible DDJ.

4.2.1.1.3 Test Method

1. Choose a supported USB4 speed to start with in the Generator.
2. Configure Generator transmit PRBS15 with SSC turned on and all jitter components turned off.
3. Configure Generator amplitude to fit into the Receiver Eye Mask.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.

5. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be spectral.
    - Adjust vertical scale to fit signal into scope screen.
6. Configure the Generator to preset 0.
7. Measure DDJ and Register it.
8. Repeat the measurement for all 16 presets, except preset 15 that are in Table 3-5.
9. Configure the Generator to the preset that provides the lowest DDJ.

4.2.1.2 AC Common Mode Measurements

4.2.1.2.1 Reference

AC_CM - USB4 Specification section 3.5.2.

4.2.1.2.2 Requirement

AC common mode voltage - 100mVp-p.

4.2.1.2.3 Test Method

1. Configure Generator transmit PRBS31 with SSC turned on and all jitter components turned off.
2. Configure Signal generator frequency to 400MHz, turn sinusoidal output on.
3. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - Adjust vertical scale to fit signal into scope screen.
    - No CDR, no average and no interpolation to be used.
    - Scope BW shall be as specified in 6.1.

4. $V_{AC\text{-}CM}=(V_{TX\text{-}P}+V_{TX\text{-}N})/2$
5. Configure Signal generator amplitude to get $V_{AC\text{-}CM}$ of 100mVp-p.
6. Turn off the ACCM noise before calibrating the RJ.

4.2.1.3 Random Jitter

4.2.1.3.1 Reference

RJ - USB4 Specification Tables 3-11, 3-12.

4.2.1.3.2 Requirement

Random Jitter - 0.14UIp-p Referenced to 1E-12 statistics.

4.2.1.3.3 Test Method

1. Turn Generator RJ generator on (PRBS15) with SSC on.
2. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
3. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be spectral.
    - Adjust vertical scale to fit signal into scope screen.
    - Removing intrinsic scope noise and jitter is recommended.
    - Note, RJp-p = RJrms * 14.
4. Tune RJ in the Generator to reach 0.14UIp-p over the oscilloscope.
5. Turn off the RJ noise before calibrating the PJ.

4.2.1.4 Periodic Jitter

4.2.1.4.1 Reference

PJ - USB4 Specification Tables 3-11, 3-12.

4.2.1.4.2 Requirement

Periodic Jitter - 0.17UIp-p.

4.2.1.4.3 Test Method

1. Turn Generator sinusoidal jitter (PJ) frequency to 1MHz generator on (PRBS15) with SSC on.

2. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
3. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
   - Pattern length -> Periodic
   - Jitter separation method shall be spectral.
   - Adjust vertical scale to fit signal into scope screen.
   - Removing intrinsic scope noise and jitter is recommended.
4. Tune PJ in the Generator to reach 0.17UIp-p over the oscilloscope (PJp-p).

4.2.1.5 Total Jitter

4.2.1.5.1 Reference

TJ - USB4 Specification Tables 3-11, 3-12.

4.2.1.5.2 Requirement

Total Jitter - 0.35UIp-p for Gen2, 0.38UIp-p for Gen3, Referenced to BER = 1E-12 statistics.

4.2.1.5.3 Test Method

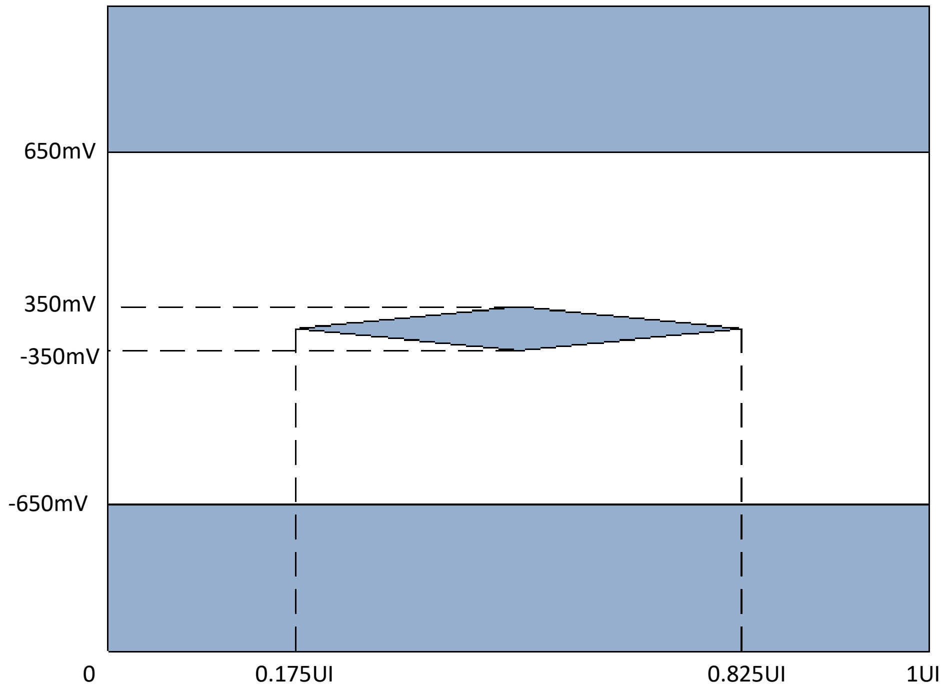
1. Configure Generator transmit PRBS15 with SSC turned on and all jitter components turned on including the ACCM noise.
2. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
3. Capture the waveform and process it with the oscilloscope:
   - Sampling Rate ≥ 80GSa/s.
   - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
   - Pattern length -> Periodic.
   - Jitter separation method shall be spectral.
   - Adjust vertical scale to fit signal into scope screen.
   - Referenced to 1E-12 statistics.
   - Removing intrinsic scope noise and jitter is recommended.

4. Verify TJ of 0.35 ± 0.0125UIp-p for Gen2 or 0.38 ± 0.025UIp-p for Gen3, if not, tune the total jitter by adjusting the RJ component for PJ frequencies 1MHz, 2MHz, 10 MHz and 50MHz and by adjusting the PJ component for frequency of 100MHz.

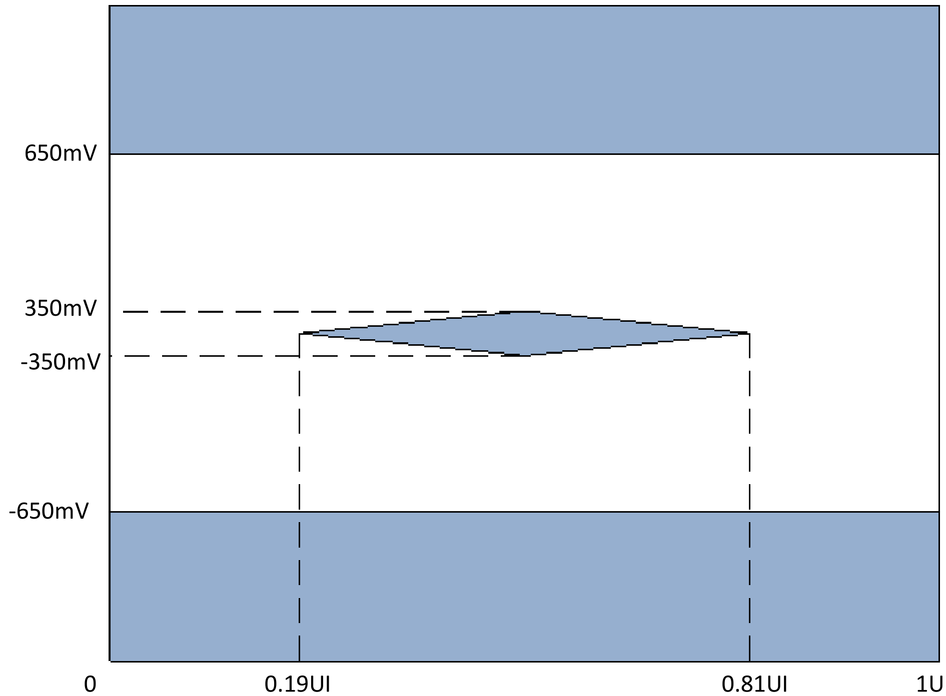
4.2.1.6 Input Eye Diagram

4.2.1.6.1 Reference

USB4 Specification Tables 3-11, 3-12.



Router Assembly Gen2 Input Eye Notation



Router Assembly Gen3 Input Eye Notation

4.2.1.6.2 Requirement

Inner Eye height of 700mVp-p.

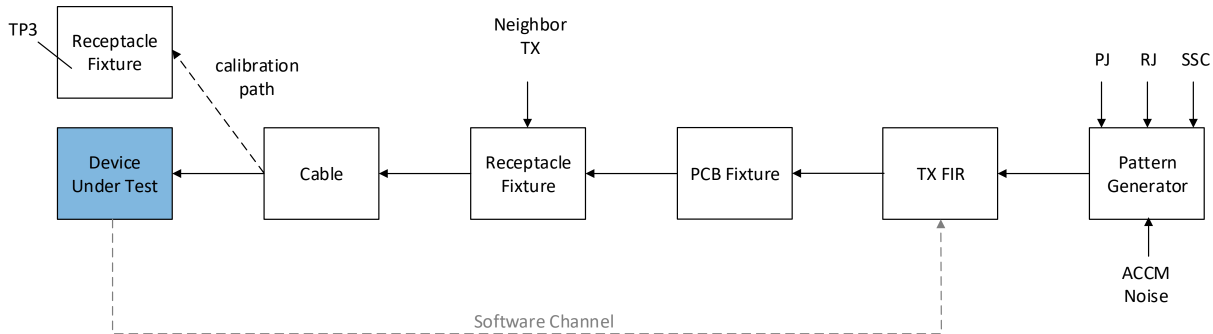
4.2.1.6.3 Test Method

1. Configure Generator transmit PRBS31 with SSC turned on and all jitter components turned on including the ACCM noise.
2. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
3. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.
4. Compare the data eye to the TP3’ eye diagram mask:
    i. If any part of the waveform hits the mask or if the eye is not minimal, Tune the Generator amplitude.
5. If Amplitude tune was needed, check all jitter and noise components again.
6. Save setup for the chosen speed TP3’, min eye as **Test Case 1**.

7. Repeat the steps in 4.2.1.4 Periodic Jitter, 4.2.1.5 Total Jitter and 4.2.1.6 Input Eye Diagram above for all PJ frequencies: 1MHz, 2MHz, 10MHz, 50MHz and 100MHz.
8. Repeat the calibration sections 4.2.1.1 to 4.2.1.6 above for all supported USB4 Gen2 and Gen3 speeds.

4.2.2 Stressed Electrical Signal for TP3 (Case 2):

Case 2:



- The trace added shall be calibrated according to Appendix C.
- Cables connecting from the last receptacle to the scope shall be de-embedded. The length of the cable shall be maximum 1m.
- TP3 Calibration start point is TP3' test setup that was calibrated for **Test Case 1**.

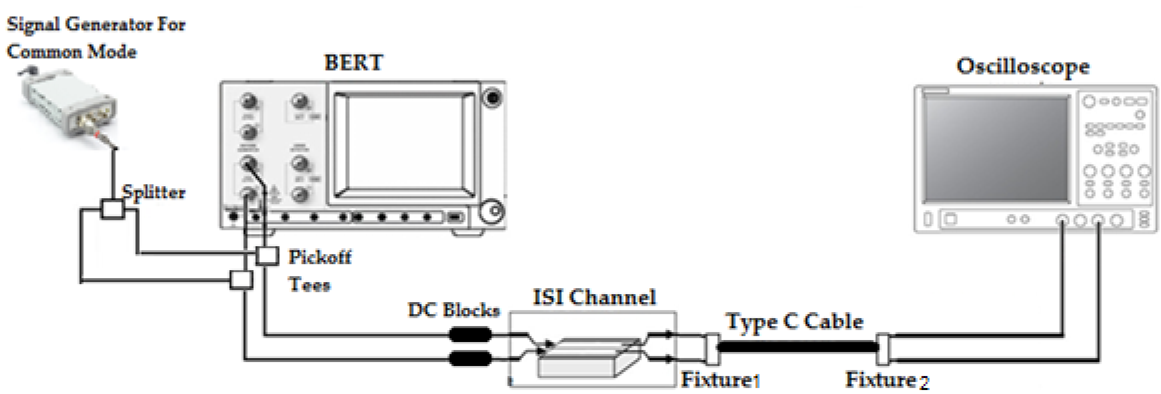


Figure 7. Receiver Calibration Setup – TP3

Note:

1. *The Pickoff Tee shall be connected directly to the Generator out. The DC block shall be connected to the Pickoff Tee directly.*
2. *As there is no reliable solution for Total Jitter measurements at TP3 in the market, calibration is done referring to Eye measurements only.*

4.2.2.1 Input Eye Diagram

4.2.2.1.1 Reference

USB4 Specification Tables 3-11, 3-12.

4.2.2.1.2 Requirement

As there is no reliable solution for Total Jitter measurements at TP3 in the market, calibration is done referring to Eye measurements only:

Inner eye height within 120 ± 10mV diff p-p for Gen2, 98 ± 10mV diff p-p for Gen3.

Eye width within 0.58 ± 0.025UI p-p for Gen2, 0.54 ± 0.05UI p-p for Gen3.

4.2.2.1.3 Test Method

1. Choose a supported USB4 speed to start with.
2. Recall Test Case 1 calibrated setup that was saved in section 4.2.1.6.3.
3. Configure Generator transmit PRBS31 with SSC turned on and all jitter components turned on including the ACCM noise (as tuned for TP3').
4. Configure the Generator to preset 0.
5. Measurement shall be done with a calibrated reference equalizer (CTLE and DFE) as described in Appendix B.
6. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
7. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Adjust vertical and horizontal scale to fit signal into scope screen.
    - Measured 1E6 UI.

8. Capture Eye height and Eye width:
    a. Eye height shall be at the "0" of the real time eye horizontal position. A Histogram shall be applied to the lower and upper section of the eye, with +/-1% deviation in time axis in order to calculate the eye height. Use min value from upper histogram result and max value from lower histogram results. Eye height is the delta between them. See Figure 15 and Figure 16. If scope Eye Height measurement calculate with the same window, you can skip the above and use Eye Height scope measurement.
    b. Eye Height and Eye Width measurements shall be executed 5 times and the median measurement shall be used.
        - If needed, tune the Eye height to be 120 ± 10mV diff p-p for Gen2 and 98 ± 10mV diff p-p for Gen3 by adjusting the Generator amplitude.

- If needed, tune the Eye width to 0.58 ± 0.025UIp-p for Gen2, 0.54 ± 0.05UIp-p for Gen3 by adjusting the PJ component for frequency of 100MHz and adjusting the RJ component for all other frequencies 1MHz, 2MHz, 10MHz and 50MHz.

9. Save **Test Case 2**.
10. Repeat the steps above for all the PJ frequencies: 1MHz, 2MHz, 10MHz, 50MHz and 100MHz.
11. Repeat the calibration above for all supported USB4 Gen2 and Gen3 speeds.

4.3 Receiver BER Test Procedure

After calibrating the stressed eye as described in the previous section, follow the following procedure to test the Receiver which is done with an internal error detector.

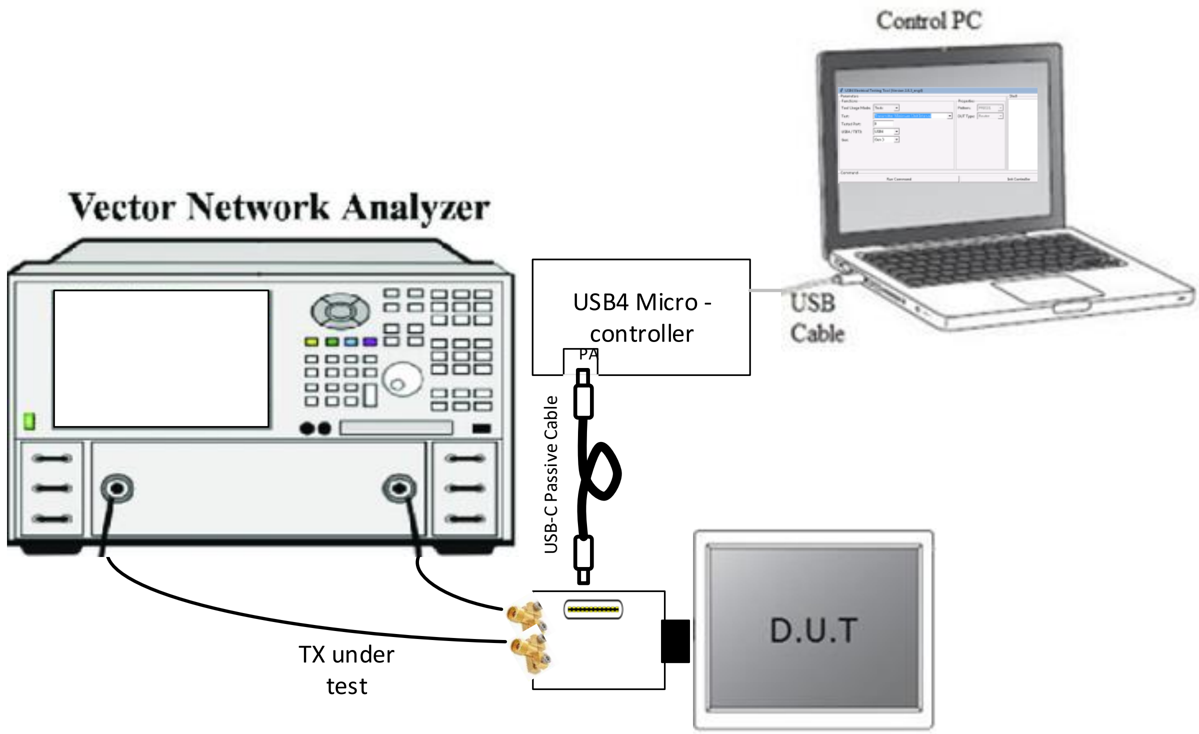


Figure 8. Receiver Test Setup at TP3'

4.3.1 BER at TP3'

4.3.1.1 Reference

Refer to Figure 8 to connect the DUT for Receiver testing.

4.3.1.2 Test Method

- Plug the test fixture into the DUT Receptacle.
    i. Connect the positive lane that was connected to the scope during calibration to Lane under test of the test fixture positive lane.
    ii. Connect the negative lane that was connected to the scope during calibration to Lane under test of the test fixture negative lane.
    iii. Connect a USB Type-C Passive cable from the Passive receptacle connector over the test fixture to the Low speed united coupon Passive receptacle connector that is connected to the USB4 Micro-controller PA.

iv. Connect a USB Type-C Active cable from the Active receptacle connector over the test fixture to the Low speed united coupon Active receptacle connector that is connected to the USB4 Micro-controller PA.

v. The USB4 Micro-controller is connected to control PC via USB cable, running the latest USB4 SW Electrical Test Tool (ETT).

1. Recall Test Case 1 calibrated setup that was saved in section 4.2.1.6.3.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. Initiate negotiation with the preset chosen in calibration.
4. Change the preset in the Generator according to the newly requested preset.
5. Configure DUT for the next negotiation step with the new preset.
6. Change the preset in the Generator according to the newly requested preset.
7. Configure DUT for the next negotiation step with the new preset.
8. If needed, change the preset in the Generator according to the newly requested preset.
9. Run BER test for 30sec. Inject 3 errors from the Generator. Read the error counter, if the error count = 3 than continue to next step, otherwise check if the lane under test is connected to the Generator or any other setup issue.
10. Repeat steps 1-8 above again and then:
11. Run BER test.
12. Record error count for 10sec.
13. Repeat steps 1-8 three more times and record the error count for 10 secs for each cycle (you will have a Total error count of 4 cycles).
14. Repeat steps 1-8 above again and then:
15. Record the error count for Gen2 400sec and Gen3 200sec.
16. Verify the error count.
17. If the error count = 0 then **Pass**.
18. If error count > 0 then run steps 1 – 9 and then:
19. Record error count for Gen2 700sec and Gen3 350sec
20. If error count > 1 then **Fail**.
21. Repeat all the steps above for each PJ: 1MHz, 2MHz, 10MHz, 50MHz and 100MHz.
22. Repeat all the steps above for each Lane (each lane must be tested using each PJ frequency).
23. Repeat the test above for all supported USB4 Gen2 and Gen3 speeds (For each supported speed, every lane must be tested for every PJ frequency.)

4.3.2 BER at TP3

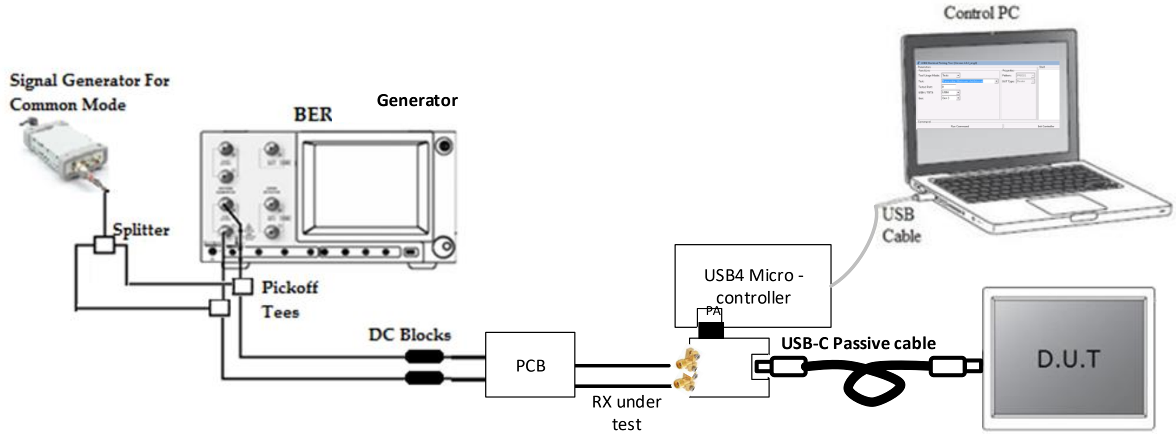


Figure 9. Receiver Test Setup at TP3

4.3.2.1 Reference

Refer to Figure 9 to connect the DUT for Receiver testing.

4.3.2.2 Test Method

- Remove Test fixture 2 (from the calibration setup).
- Plug the USB Type-C cable into the DUT Receptacle.
- Connect the Test fixture plug directly to the USB4 Micro-controller PA.
- The USB4 Micro-controller is connected to control PC via USB cable, running the latest USB4 SW Electrical Test Tool (ETT).

1. Recall Test Case 2 calibrated setup that was saved in section 4.2.2.1.3.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. Initiate negotiation with the preset 0.
4. Change the preset in the Generator according to the newly requested preset.
5. Configure DUT for the next negotiation step with the new preset.
6. Change the preset in the Generator according to the newly requested preset.
7. Configure DUT for the next negotiation step with the new preset.
8. If needed, change the preset in the Generator according to the newly requested preset.

9. Run BER test for 30sec. Inject 3 errors from the Generator. Read the error counter, if the error count = 3 than continue to next step, otherwise check if the lane under test is connected to the Generator or any other setup issue.
10. Repeat steps 1-8 above again and then:
11. Run BER test.
12. Record the error count for 10sec.
13. Repeat steps 1-8 three more times and record the error count for 10 secs for each cycle (you will have a Total error count of 4 cycles).
14. Repeat steps 1 -8 above again and then:
15. Record the error count for Gen2 400sec and Gen3 200sec.
16. Verify the error count.
17. If count = 0 then **Pass**.
18. If error count > 0 then run steps 1 – 9 and then:
19. Record the error count for Gen2 700sec and Gen3 350sec.
20. If error count > 1 then **Fail**.
21. Repeat all the steps above for each PJ: 1MHz, 2MHz, 10MHz, 50MHz and 100MHz.
22. Repeat all the steps above for each Lane (each lane must be tested using each PJ frequency).
23. Repeat the test above for all supported USB4 Gen2 and Gen3 speeds (For each supported speed, every lane must be tested for every PJ frequency.)

4.4 Receiver Multi Error-Burst Test

4.4.1 Multi Error-Burst Gen3

4.4.1.1 Reference

USB4 Specification section 3.5.3.

4.4.1.2 Requirement

The probability for obtaining a capture containing an error burst followed by one or more correct bits and then by error burst restart, shall not exceed 5E-7 (i.e. one error burst restart per 2 million error captures on average).

4.4.1.3 Test Objective

Confirm that the Multi Error-Burst falls within the limits of the USB4 Specification.

Notes:

i. *The test is applicable only to the nearest receiver Re-Timer Device to the USB Type-C connector.*
ii. *This test is normative in case that the nearest Retiming Device to the USB Type-C connector has more than 1 DFE TAP.*

4.4.1.4 Test Method

1. Use the same test setup as in Figure 9.
2. Choose a supported USB4 Gen3 speed.
3. Recall Test Case 2 calibrated setup for PJ 100MHz that was saved in section 4.2.2.1.3.
4. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
5. Initiate negotiation with the preset 0.
6. Change the preset in the Generator according to the newly requested preset.
7. Configure DUT for the next negotiation step with the new preset.
8. Change the preset in the Generator according to the newly requested preset.
9. Configure DUT for the next negotiation step with the new preset.
10. If needed, change the preset in the Generator according to the newly requested preset.
11. Increase the PJ magnitude to the point where Uncoded BER of 1E-8 is observed.
12. Run BURST BER TEST and record Burst Count and Error Events after 20,000sec for Gen3.
13. If the probability for obtaining a capture containing an error burst followed by one or more correct bits and then by error burst restart ≤ 5E-7 then **Pass**.

14. If the probability for obtaining a capture containing an error burst followed by one or more correct bits and then by error burst restart > 5E-7 then **Fail flow** is permitted, Trading Burst Renewal probability with Uncoded BER. Detailed performance is listed below. For example, if the measured Burst Renewal probability is 5E-6 than the DUT shall achieve Uncoded BER performance of 1E-13:

$\Pr\{Burst\ Renewal\}$	Uncoded BER
5E-7	**1E-12**
5E-6	1E-13
5E-5	1E-14
5E-4	1E-15

15. In order to achieve the new BER target as above, repeat section 4.3.2 for the following time test:

Uncoded BER	Gen3 BER Test Time [s]
1E-13	3500
1E-14	35000
1E-15	350000

16. If DUT BER >0 then **Fail**.
17. Repeat all the steps above for each Lane.

4.5 Receiver Signal Frequency Variations Training Test

4.5.1 Signal Frequency Variations Training Gen2 and Gen3

4.5.1.1 Reference

USB4 Specification table 3-10.

4.5.1.2 Requirement

The probability for BER shall be 1E−6 or less.

4.5.1.3 Test Objective

Confirm that the Receiver don’t lose lock and record errors when frequency variation is applied.

4.5.1.4 Test Method

1. Due to test equipment limitation, emulation to the frequency variation will be applied.
2. Use the same test setup as in Figure 9.
3. Choose a supported USB4 speed to start with.
4. Recall Test Case 2 calibrated setup for PJ 1MHz that was saved in section 4.2.2.1.3.
5. Turn off Generator SSC modulation and change PJ frequency to be 0.4MHz with amplitude of 22.4UI for Gen2 or 44.8UI for Gen3. All other ingredients stay the same.
6. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
7. Initiate negotiation with the preset 0.
8. Change the preset in the Generator according to the newly requested preset.
9. Configure DUT for the next negotiation step with the new preset.
10. Change the preset in the Generator according to the newly requested preset.
11. Configure DUT for the next negotiation step with the new preset.
12. If needed, change the preset in the Generator according to the newly requested preset.
13. Run BER test.
14. Record Error Count for 10sec.
15. If BER ≤ 1E-6 then **Pass**.
16. If BER> 1E-6 then **Fail**.
17. Repeat all the steps above for each Lane.
18. Repeat the test above for all supported USB4 Gen2 and Gen3 speeds.

4.6 Receiver Return Loss Gen2 and Gen3

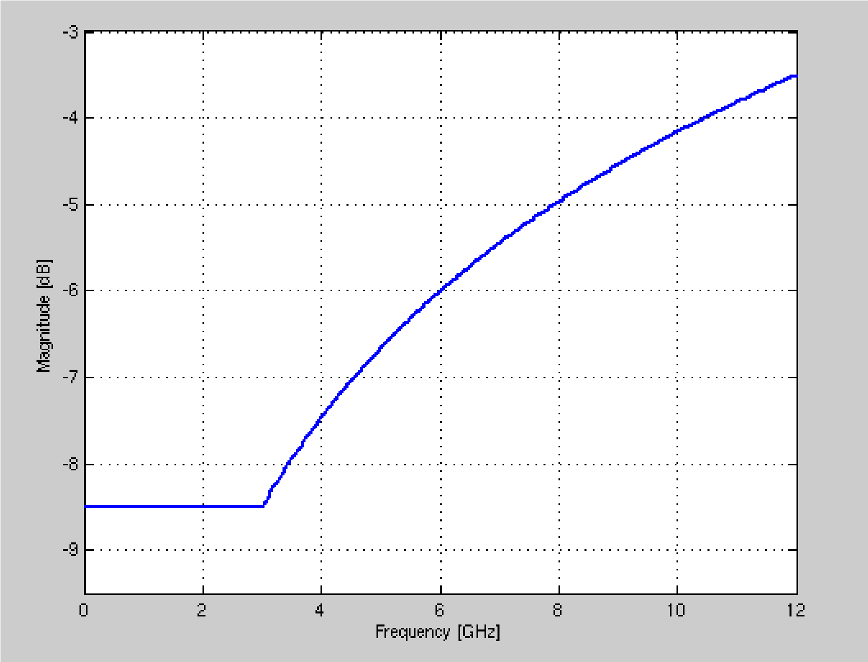
4.6.1 Receiver Differential Return Loss

4.6.1.1 Reference

USB4 Specification Table 3-10.

4.6.1.2 Requirement

$$\mathrm{SDD22(f)} = \begin{cases} -8.5 & 0.05 < f_{GHz} \leq 3 \\ -3.5 + 8.3 \cdot \log 10\left(\frac{f_{GHz}}{12}\right) & 3 < f_{GHz} \leq 12 \end{cases}$$



4.6.1.3 Test Objective

Confirm that the Return loss falls within the limits of the USB4 Specification.

4.6.1.4 Test Method

1. Choose a supported USB4 speed to start with.
2. Configure DUT transmitter to output PRBS31 and enable RX on all lanes with SSC turned on.
3. Setup the Network Analyzer with measurement:

   Note: *As the extra loss and distortion elements may be compensated by physical or mathematical means, use gating option to compensate for the parts of the fixture that are with 100ohm impedance. No de-embedding or gating the mated USB Type-C connector or the TF traces:*

- *Frequency range of 50MHz to 12GHz.*
- *IF BW of 1KHz.*
- *At least 1600 points.*
- *Impedance 85 ohm differential.*
- *Define the Topology to Bal.*
- *Define the measurement at SDD22.*

4. Calibrate the network analyzer using a 2-port auto calibration kit.
5. Connect a USB Type-C Passive cable from the Passive receptacle connector over the test fixture to the Low speed united coupon Passive receptacle connector that is connected to the USB4 Micro-controller PA.
6. Connect a USB Type-C Active cable from the Active receptacle connector over the test fixture to the Low speed united coupon Active receptacle connector that is connected to the USB4 Micro-controller PA.
7. Connect Lane under test RX_P, RX_N to the Network Analyzer.

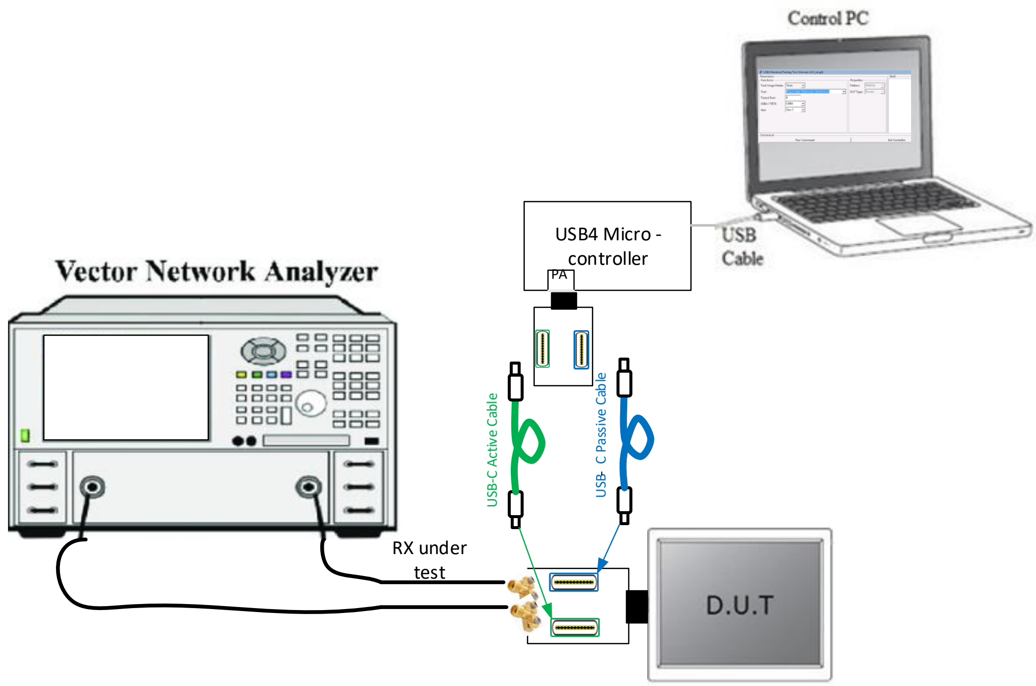


Figure 10. Receiver Return Loss Test Setup

8. Measure the Return Loss with the Network Analyzer.
9. If Return loss violated the above requirement, then **Fail.**
10. Repeat the test for all remaining USB4 lane.
11. Repeat the test for all supported USB4 Gen2 and Gen3 speeds.

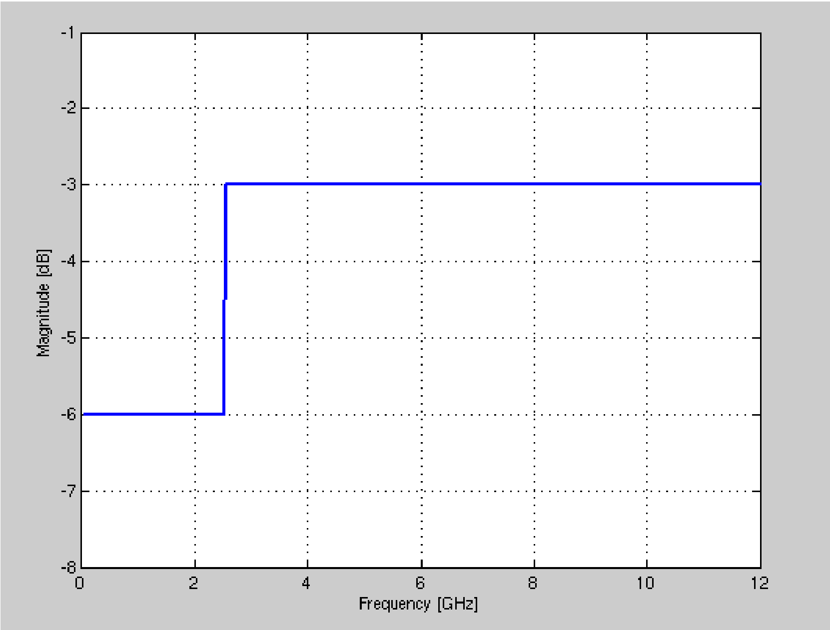
4.6.2 Receiver Common Mode Return Loss

4.6.2.1 Reference

RL_COMM - USB4 Specification Table 3-10.

4.6.2.2 Requirement

$$\text{SCC22(f)} = \begin{cases} -6 & 0.05 < f_{GHz} \le 2.5 \\ -3 & 2.5 < f_{GHz} \le 12 \end{cases}$$



4.6.2.3 Test Objective

Confirm that the Common Mode Return loss falls within the limits of the USB4 Specification.

4.6.2.4 Test Method

1. Choose a supported USB4 speed to start with.
2. Configure DUT transmitter to output PRBS31 and enable RX on all lanes with SSC turned on.
3. Setup the Network Analyzer with measurement:

   Note: *As the extra loss and distortion elements may be compensated by physical or mathematical means, use gating option to compensate for the parts of the fixture that are with 100ohm impedance. No de-embedding or gating the mated USB Type-C connector or the TF traces:*

   - *Frequency range of 50MHz to 12GHz.*
   - *IF BW of 1KHz.*

- *At least 1600 points.*
- *Impedance 85 ohm differential.*
- *Define the Topology to Bal.*
- *Define the measurement at SCC22.*

4. Calibrate the network analyzer using a 2-port auto calibration kit.
5. Connect a USB Type-C Passive cable from the Passive receptacle connector over the test fixture to the Low speed united coupon Passive receptacle connector that is connected to the USB4 Micro-controller.
6. Connect a USB Type-C Active cable from the Active receptacle connector over the test fixture to the Low speed united coupon Active receptacle connector that is connected to the USB4 Micro-controller.
7. Connect Lane under test RX_P, RX_N to the Network Analyzer.
8. Measure the Return Loss with the Network Analyzer.
9. If Return loss violated the above requirement, then **Fail**.
10. Repeat the test for all remaining USB4 lane.
11. Repeat the test for all supported USB4 Gen2 and Gen3 speeds.

5 Router Assembly Sideband Signal Testing

The Sideband link is used for initial link configuration.

Sideband Link transactions are sent and received over the SBTX and SBRX wires.

The SBTX/SBRX lines are connected to SBU1/SBU2 on the USB Type-C connector after negotiation to the USB4 Alternate Mode has occurred. SBTX and SBRX are a UART interface which operates at a 1MHz baud rate. The local UART bus between Router Assembly and near cable end or Device and near cable end shall function when high speed USB4 traffic is present to enable debug and link training to work properly.

Figure 11 indicates the Low-Speed signals for a Router Assembly.

All Router Assembly shall meet the Low-Speed Link requirements defined section 5.1.

Sideband signal parameters are defined at the connector.

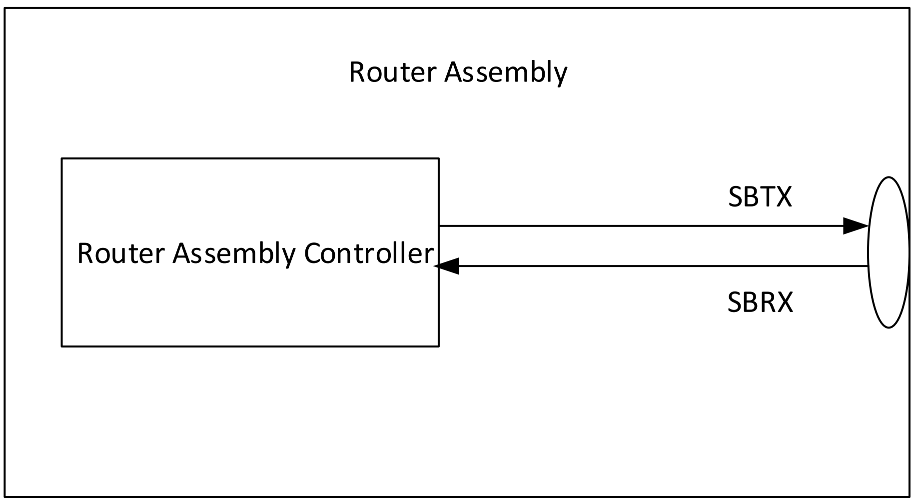


Figure 11. Router Assembly Low-Speed Signals

5.1 Router Assembly SBTX and SBRX Specifications

Low Speed Signal testing shall be done for each port in the DUT.

The SBTX, SBRX signals are going through SBU1, SBU2 pins over the USB Type-C connector.

For all setups the DUT shall be connected via USB4 Test Fixture with USB4 μ-controller in order to establish link.

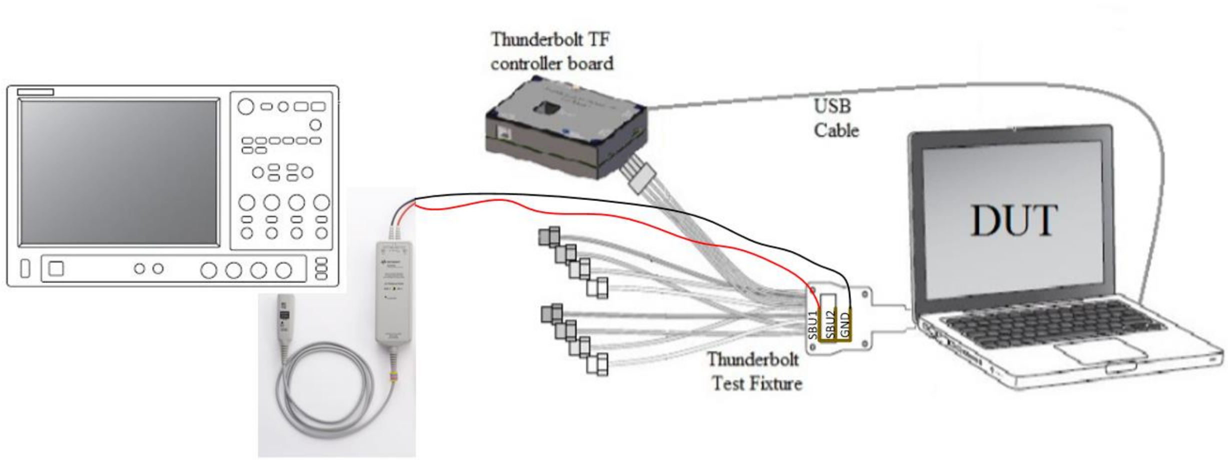


Figure 12. Figure of Sideband TX/RX Signal Testing Using Scope

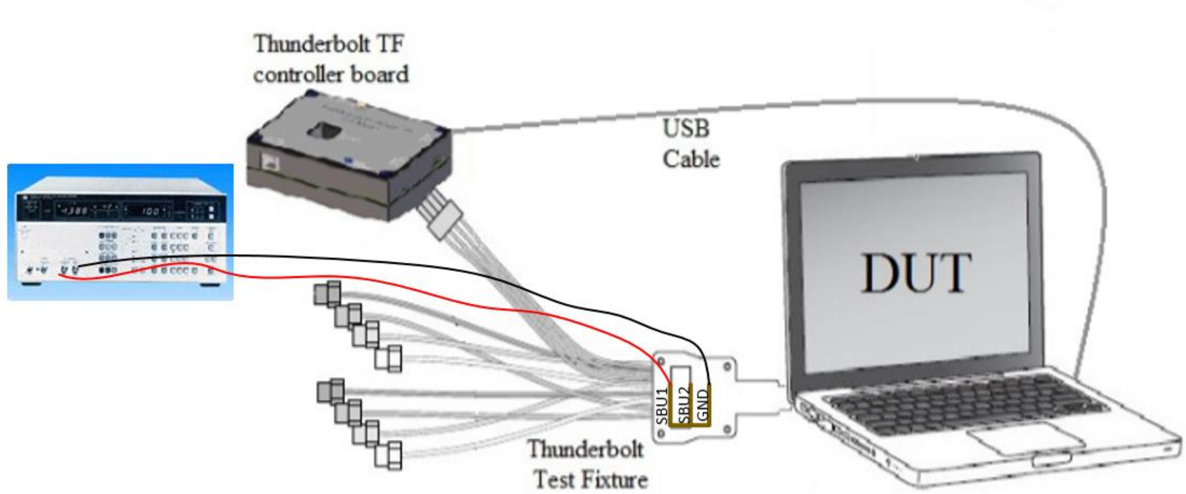


Figure 13. Figure of Sideband TX/RX Signal Testing Using Voltage Meter

5.1.1 SBTX High Voltage Measurement

5.1.1.1 Reference

$SBTX_{VOH}$ - USB4 Specification Table 3-1.

5.1.1.2 Requirement

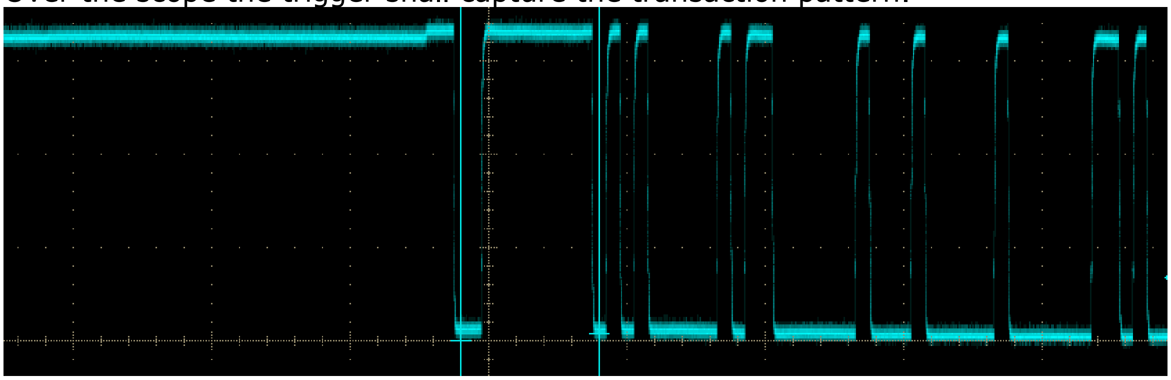
$2.4V \leq SBTX_{VOH} \leq 3.47V$.

5.1.1.3 Test Objective

Confirm that the parameter measured under all conditions does not exceed minimum or maximum limits of the specification.

5.1.1.4 Test Method

1. Connect a voltage meter/fluke to SBU1 header in the USB4 Test Fixture (Figure 13).
2. Power up the DUT.
3. Measure the voltage.
4. If $SBTX_{VOH}$ < 2.4V or >3.47V then Fail.
5. Connect a scope with high impedance probe to the SBU1 header in the USB4 Test Fixture (Figure 12).
   - In the scope use a trigger based on pulse width, negative polarity, trigger when the pulse width <10μs and threshold of 600mV.
   - Horizontal scale = 10μs per square, vertical scale = 1V per square.
6. Power up the DUT.
7. Over the scope the trigger shall capture the transaction pattern.



8. Measure the high/low value of the “1” amplitude for a bit inside the transaction. Over/undershoot shall be ignored.
9. If $SBTX_{VOH}$ < 2.4V or >3.47V then Fail.

5.1.2 SBTX Low Voltage Measurement

5.1.2.1 Reference

$SBTX_{VOL}$ - USB4 Specification Table 3-1.

5.1.2.2 Requirement

$-0.05V \leq SBTX_{VOL} \leq 0.4V$.

5.1.2.3 Test Objective

Confirm that the parameter measured under all conditions does not exceed minimum or maximum limits of the specification.

5.1.2.4 Test Method

1. Connect a voltage meter/fluke to SBU1 header in the USB4 Test Fixture (Figure 13).
2. DUT shall be in power down.
3. Measure the voltage.
4. If $SBTX_{VOL}$ < -0.05V or >0.4V then Fail.
5. Connect a scope with high impedance probe to the SBU1 header in the USB4 Test Fixture (Figure 12).
   - In the scope use a trigger based on pulse width, negative polarity, trigger when the pulse width <10µs and threshold of 600mV.
   - Horizontal scale = 10µs per square, vertical scale = 1V per square.
6. Power up the DUT
7. Over the scope the trigger shall capture the transaction pattern.
8. Measure the high/low value of the "0" amplitude for a bit inside the transaction. Over/undershoot shall be ignored.
9. If $SBTX_{VOL}$ < -0.05V or >0.4V then Fail.

5.1.3 SBTX Rise/Fall time Measurement

5.1.3.1 Reference

SBX_{TRTF} - USB4 Specification Table 3-1.

5.1.3.2 Requirement

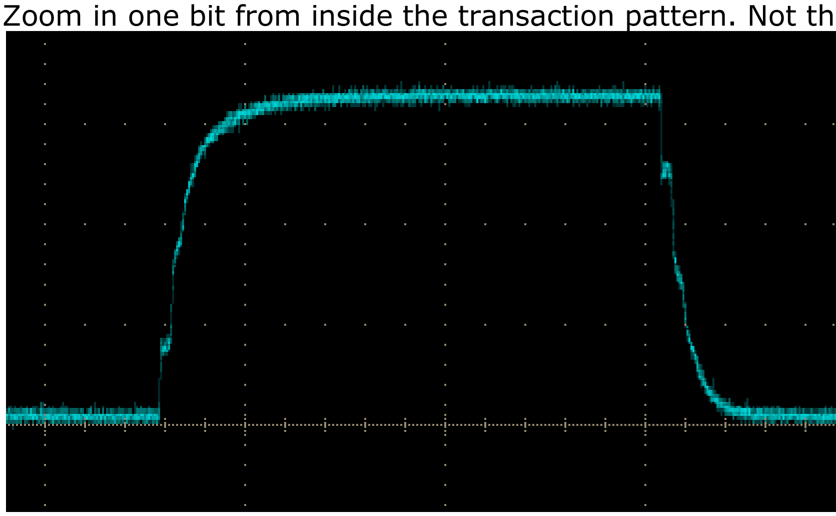
3.5ns ≤ SBX_{TRTF}

5.1.3.3 Test Objective

Confirm that the parameter measured under all conditions does not exceed minimum or maximum limits of the specification.

5.1.3.4 Test Method

1. Connect the DUT via USB4 Test Fixture with USB4 u-controller in order to establish link.
2. The measurement shall be in transaction only and not from power down to up (or the opposite).
3. Connect a scope with high impedance probe to the SBU1 header for SBTX test in the USB4 Test Fixture (Figure 12).
   - In the scope use a trigger based on pulse width, negative polarity, trigger when the pulse width <10µs and threshold of 600mV.
   - Horizontal scale = 10µs per square, vertical scale = 1V per square.
4. Power up the DUT.
5. Over the scope the trigger shall capture the transaction pattern.
6. Zoom in one bit from inside the transaction pattern. Not the 1st or the last bit.



7. Measure the rise and fall time (10%-90%) for SBTX.
8. If SBX_{TRTF} < 3.5ns then Fail.

5.1.4 SBX UI Duration Measurement

5.1.4.1 Reference

SBX_UI - USB4 Specification Table 3-1.

5.1.4.2 Requirement

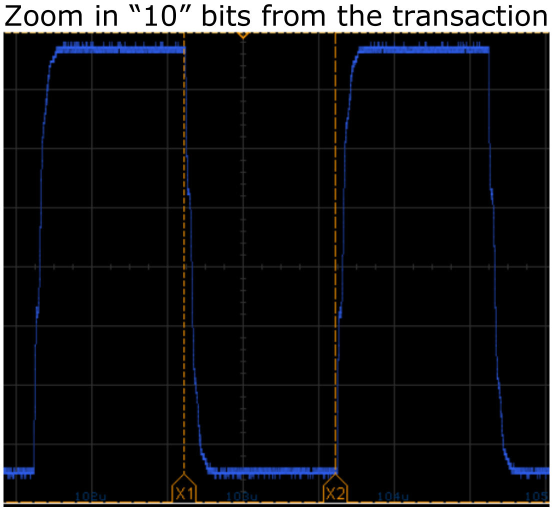
980ns ≤ SBX_UI ≤ 1020ns.

5.1.4.3 Test Objective

Confirm that the parameter measured under all conditions does not exceed minimum or maximum limits of the specification.

5.1.4.4 Test Method

1. Connect the DUT via USB4 Test Fixture with USB4 u-controller in order to establish link.
2. The measurement shall be in transaction, over the transaction pattern.
3. Connect a scope with high impedance probe to the SBU1 header for SBTX in the USB4 Test Fixture (Figure 12).
4. In the scope use a trigger based on pulse width, negative polarity, trigger when the pulse width <10µs and threshold of 600mV.
5. Horizontal scale = 10µs per square, vertical scale = 1V per square.
6. Power up the DUT.
7. Over the scope the trigger shall capture the transaction pattern.
8. Zoom in "10" bits from the transaction pattern.



9. Measure the duration from falling edge of the "1" to the rising edge of "0", named SBX_UI.
10. If SBX_UI < 980ns or >1020ns then Fail.

5.1.5 SBRX High Voltage Detection Measurement

5.1.5.1 Reference

$SBRX_{VIH}$ - USB4 Specification Table 3-1.

5.1.5.2 Requirement

2.0V ≤ $SBRX_{VIH}$ ≤ 3.72V.

5.1.5.3 Test Objective

Confirm that the DUT can detect the SBRX signal in the minimum limit of the specification.

5.1.5.4 Test Method

1. Connect the DUT via USB4 Test Fixture with USB4 u-controller with external 3.3V power supply connected to the SBX input in order to establish link.

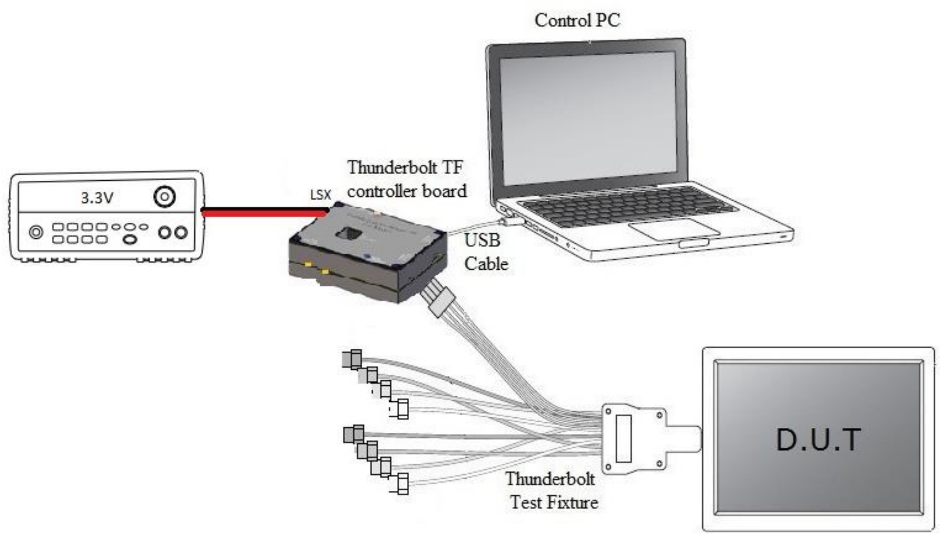


Figure 14. Figure of Sideband RX Signal Testing

2. Set the 3.3V power supply to 3.72V.
3. Establish there is a link.
4. Reduce the external power supply to 2.0V.
5. If Link is lost, then Fail.

5.1.6 SBRX Low Voltage Detection Measurement

5.1.6.1 Reference

$SBRX_{VIL}$ - USB4 Specification Table 3-1.

5.1.6.2 Requirement

$-0.3V \le SBRX_{VIL} \le 0.65V$.

5.1.6.3 Test Objective

Confirm that the DUT can detect the SBRX signal in the minimum limit of the specification.

5.1.6.4 Test Method

1. Connect the DUT via USB4 Test Fixture with USB4 u-controller with external 3.3V power supply connected to the SBX input in order to establish link (Figure 14).
2. Set the 3.3V power supply to 3.3V.
3. Establish there is a link.
4. Reduce the external power supply to 0.65V.
5. If Link is established, then Fail.

6 Appendix A – Scope Configurations

6.1 Scope BW Limit

- Gen 2 Configuration: Scope BW shall be 16 GHz.
- Gen 3 Configuration: Scope BW shall be 21 GHz.

6.2 TP3 Embedding File

- For Gen 2 use USB4_2m.s4p - the file can be found on https://usb.org/.
- For Gen 3 use USB4_0p8m.s4p - the file can be found on https://usb.org/.

7 Appendix B – Equalization Calibration

When testing at TP3, a reference equalization must be applied on the Oscilloscope. The reference receiver equalization function is comprised of parametric Continuous-Time-Linear-Equalizer (CTLE) and Decision-Feedback-Equalizer (DFE), as described in USB4 Specification section 3.3.4.1 and Section 3.3.4.2 respectively.

A measurement that is referenced to TP3 shall use the best equalization parameters so that the calculated eye-diagram is optimized.

7.1 Gen 2 Configuration

The following equation describes the frequency response for USB4 reference CTLE that shall be used with the following parameters:

$$H(s) = 1.41 \cdot \omega_{p2} \cdot \frac{s + \frac{A_{DC}}{1.41} \cdot \omega_{p1}}{(s + \omega_{p1}) \cdot (s + \omega_{p2})}$$

- $AC\ Gain\ =\ 1.41$
- $Wp1\ =\ 2 * \pi * 1.5G\frac{rad}{sec}p2\ =\ 2 * \pi * 5G\,rad/sec$
- ADC is the DC gain, Ten different CTLE configurations shall be applied such that ADC is one of $\left\{10^{\frac{-x}{20}} : x = 0 \div 9[dB]\right\}$ and shall be calibrated as follows:

1. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
2. Chose ADC for x=0.
3. Apply automatic DFE with max of 50mV. Gain shall be 1 at all time. Apply automatic DFE delay.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
    a. Sampling Rate ≥ 80GSa/s.
    b. Adjust vertical and horizontal scale to fit signal into scope screen.
    c. Measured 1E6 UI.
6. Eye height shall be at the “0” of the real time eye horizontal position. A Histogram shall be applied to the lower and upper section of the eye, with +/- 1% deviation in time axis in order to calculate the eye height. Use min value from upper histogram result and max value from lower histogram results. Eye height is the delta between them. See Figure 15 and Figure 16.

7. Capture 5 times (each time over new 1MUI record length) min value of both eye height (EH) and eye width (EW). Calculate Area by EH * EW.
8. Average the 5 Area values.
9. Start over from step #2 with x=x+1 and till x=9.

- The chosen ADC value including DFE tap value shall be the one that gives the maximal Area.
- If there are two ADC values including DFE tap with the same area, choose the one with the greater eye height.

7.2 Gen 3 Configuration

The following equation describes the frequency response for USB4 reference CTLE that shall be used with the following parameters:

$$H(s) = 1.41 \cdot \omega_{p2} \cdot \frac{s + \frac{A_{DC}}{1.41} \cdot \omega_{p1}}{(s + \omega_{p1}) \cdot (s + \omega_{p2})}$$

- $AC\ Gain$: 1.41
- $Wp1 = 2 * \pi * 5G \frac{rad}{sec} p2 = 2 * \pi * 10G\ rad/sec$
- ADC is the DC gain, Ten different CTLE configurations shall be applied such that ADC is one of $\left\{10^{\frac{-x}{20}} : x = 0 \div 9[dB]\right\}$ and shall be calibrated as follows:

1. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
2. Chose ADC for x=0.
3. Apply automatic DFE with max of 50mV. Gain shall be 1 at all time. Apply automatic DFE delay.
4. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; interpolation shall be applied for obtaining effective sample rate of 1280GSa/s (e.g. X16 interpolation for 80GSa/s hardware sampling rate). Scope BW shall be as specified in 6.1.
5. Capture the waveform and process it with the oscilloscope:
   a. Sampling Rate ≥ 80GSa/s.
   b. Adjust vertical and horizontal scale to fit signal into scope screen.
   c. Measured 1E6 UI.
6. Eye height shall be at the "0" of the real time eye horizontal position. A Histogram shall be applied to the lower and upper section of the eye, with +/- 1% deviation in time axis in order to calculate the eye height. Use min value from upper histogram result and max value from lower histogram results. Eye height is the delta between them. See Figure 15 and Figure 16.
7. Capture 5 times (each time over new 1MUI record length) min value of both eye height (EH) and eye width (EW). Calculate Area by EH * EW.
8. Average the 5 Area values.

9. Start over from step #2 with x=x+1 and till x=9.

- The chosen ADC value including DFE tap value shall be the one that gives the maximal Area.
- If there are two ADC values including DFE tap with the same area, choose the one with the greater eye height.

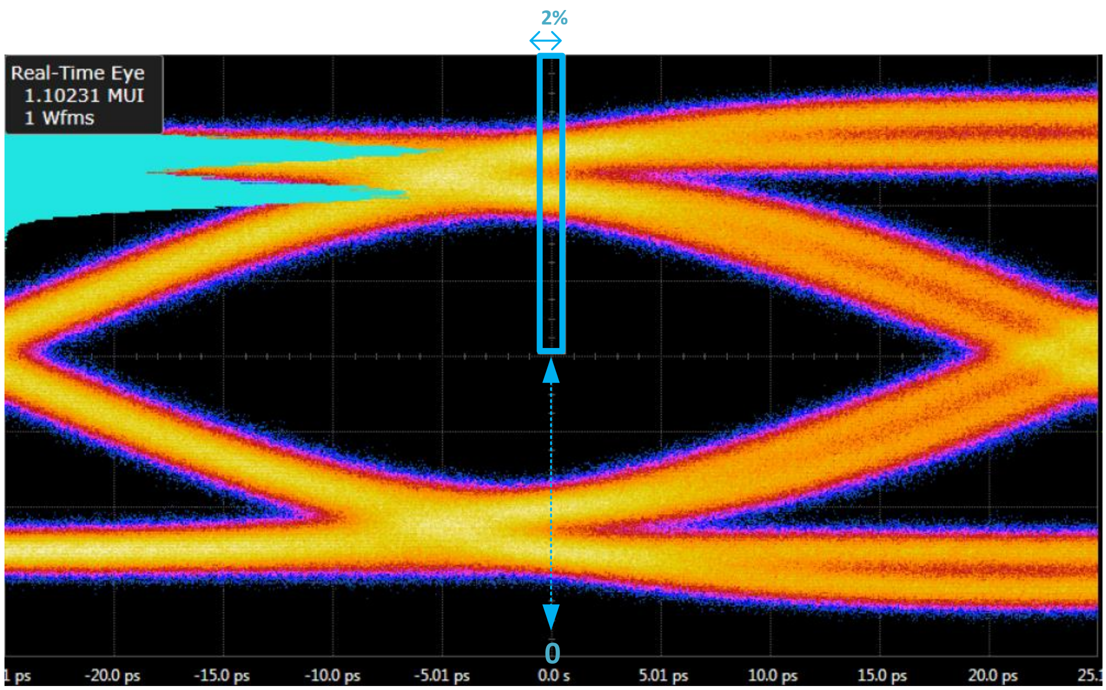


Figure 15. USB4 RX TP3 Eye Height Upper Location Measurement

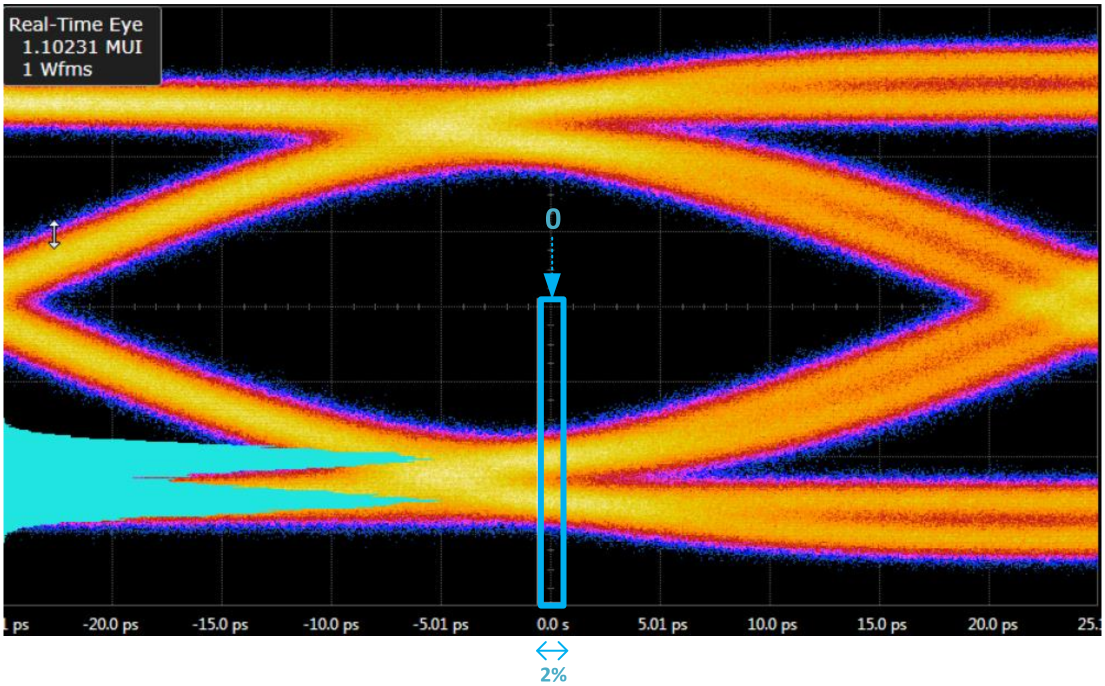


Figure 16. USB4 RX TP3 Eye Height Lower Location Measurement

8 Appendix C – USB4 RX TP3 Trace Calibration

8.1 Gen2 Configuration

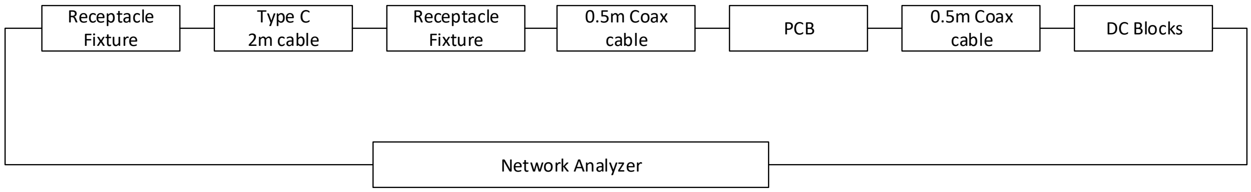


Figure 17. USB4 RX TP3 Trace Calibration Setup for Gen2

- The trace added shall be combined from a DDJ (PCB) and a USB Type-C 2m cable.
- The USB Type-C 2m cable insertion loss with the fixtures shall be 13 ± 0.5dB at 5GHz and the total channel insertion loss shall be 18 to 19 dB at 5GHz for the entire channel (after the pickoff tees till the fixture, with the DC blocks).
- IL shall be dominated by the PCB and the 2m passive cable, the length of the RF cable shall be short as possible and, in any case, no more than 1m.
- All 2 lanes shall meet the above requirement.
- The chosen lane used for RX calibration shall be the lane with minimum IL (minimum of IL absolute value).

8.2 Gen3 Configuration

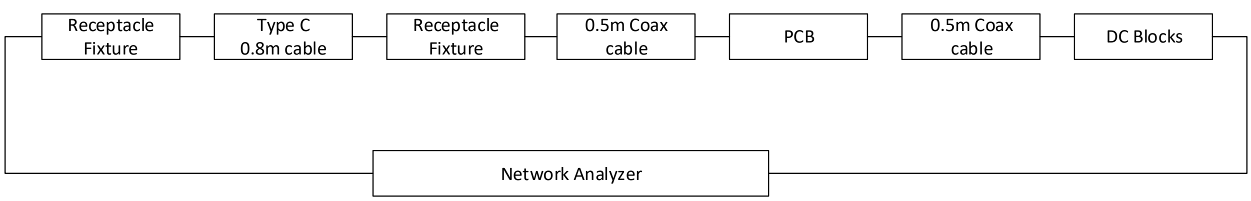


Figure 18. USB4 RX TP3 Trace Calibration Setup for Gen3

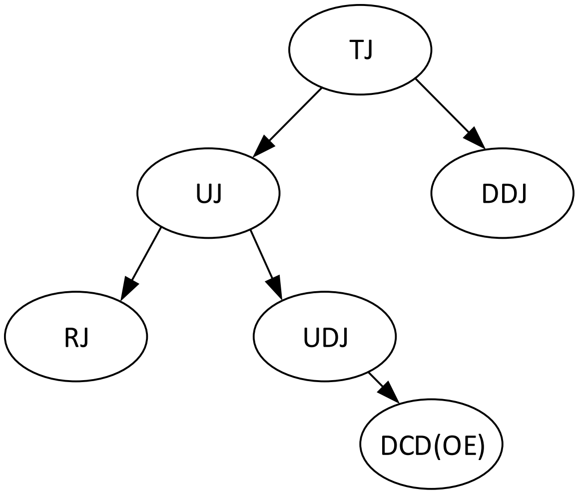
- The trace added shall be combined from a DDJ (PCB) and a USB Type-C 0.8m cable.
- The USB Type-C 0.8m cable insertion loss with the fixtures shall be 10.5 ± 0.5dB at 10GHz and the total channel insertion loss shall be 16 to 17 dB at 10GHz for the entire channel (after the pickoff tees till the fixture, with the DC blocks).
- IL shall be dominated by the PCB and the 0.8m passive cable, the length of the RF cable shall be short as possible and, in any case, no more than 1m.
- All 2 lanes shall meet the above requirement.

- The chosen lane used for RX calibration shall be the lane with minimum IL (minimum of IL absolute value).

9 Appendix D – Jitter Term Definition

9.1 High level of Total jitter decomposition

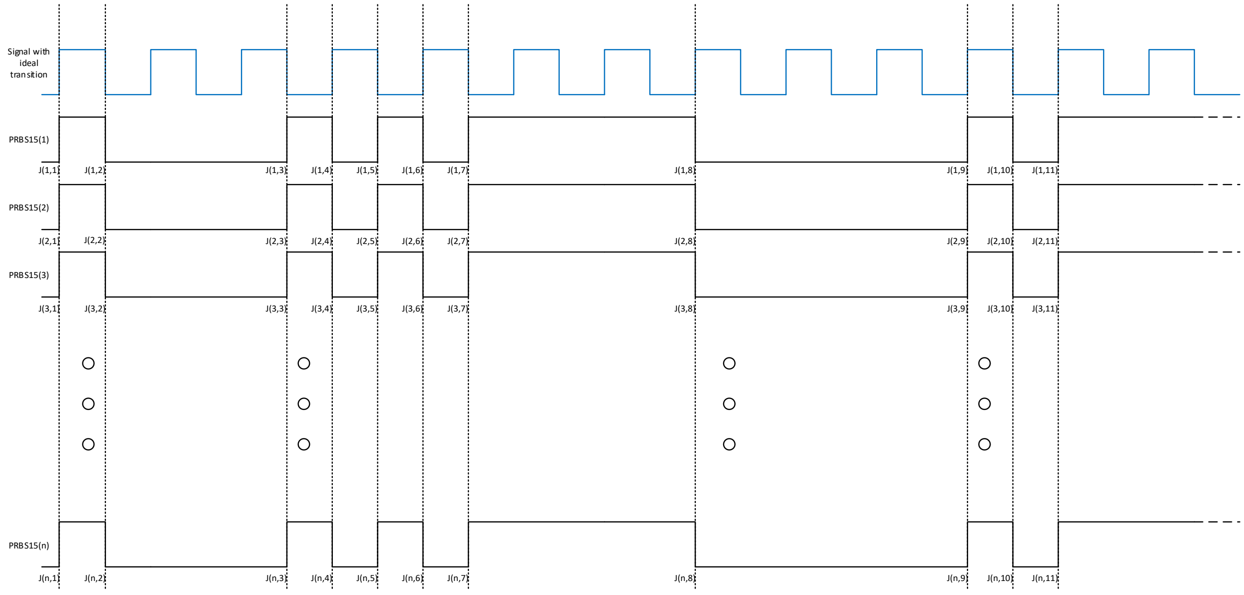
As detailed in the USB4 Specification, TJ break down will looks as the following diagram:



9.2 DDJ

A repeating PRBS15 pseudo-random test pattern is used.

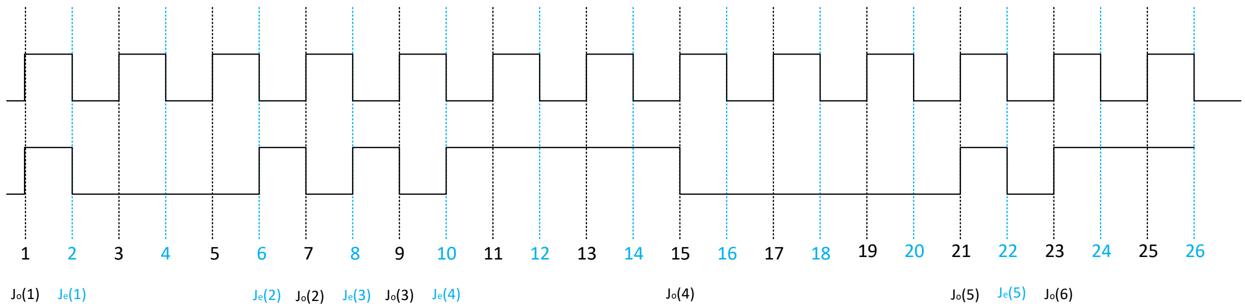
Synchronize the pattern repetition and average the crossing times sufficiently per each specific zero-crossing to remove the effects of uncorrelated jitter. The mean time of each crossing is then compared to the expected time of the crossing, and a set of 16,384 timing variations is determined. DDJ is the range (max-min) of the timing variations. Keep track of the signs (early/late) of the variations.



$$\text{DDJ} = \underset{i}{\text{Max}}\left\{\underset{m}{\text{average}}[J(m,i)]\right\} - \underset{i}{\text{Min}}\left\{\underset{m}{\text{average}}[J(m,i)]\right\}$$

9.3 DCD

DCD mean even-odd jitter. The deviation of the time of each transition from an ideal clock at the signaling rate is measured. Even-odd jitter is defined as the magnitude of the difference between the average deviation of all even-numbered transitions and the average deviation of all odd-numbered transitions, where determining if a transition is even or odd is based on possible transitions but only actual transitions are measured and averaged.



$$DCD = abs[average(Jo) - average(Je)]$$

10 Appendix E – Interoperability with Thunderbolt™ 3 (TBT3) Systems

When TBT3 Mode is established, an Adapter shall run at a TBT3-Compatible speed.

A Router Assembly shall support TBT3-Compatible Gen2 Speed (10.3125Gbps). A Router Assembly may also optionally support TBT3-Compatible Gen3 Speed (20.625Gbps).

10.1 Gen2 and Gen3 Router Assembly Transmitter Compliance

A Router Assembly Transmitter shall meet the specifications described in this CTS, except for the following set of parameters that shall be used instead of the values specified above. Listed just the gap from the above in section 3 and 4.

10.1.1 Gen2 and Gen3 SSC Down Spread Rate Measurement

10.1.1.1 Reference

SSC_Down_Spread_Rate - USB4 Specification Table 13-1.

10.1.1.2 Requirement

35KHz ≤ SSC_Down_Spread_Rate ≤ 37KHz.

Test Objective and Test Method stay the same.

10.1.2 Gen2 and Gen3 SSC Phase Deviation Measurement

10.1.2.1 Reference

SSC_Phase_Deviation - USB4 Specification Table 13-1.

10.1.2.2 Requirement

2.5ns p-p ≤ SSC_Phase_Deviation ≤ 18.5ns p-p.

Test Objective and Test Method stay the same.

10.1.3 Gen2 Minimum Unit Interval Measurement

10.1.3.1 Reference

UI USB4 Specification Table 13-1.

10.1.3.2 Requirement

96.9406ps ≤ Minimum Unit Interval ≤ 96.9988ps.

Test Objective and Test Method stay the same.

10.1.4 Gen3 Minimum Unit Interval Measurement

10.1.4.1 Reference

UI USB4 Specification Table 13-1.

10.1.4.2 Requirement

48.4703ps ≤ Minimum Unit Interval ≤ 48.4994ps.

Test Objective and Test Method stay the same.

10.1.5 Gen2 Average Unit Interval Measurement

10.1.5.1 Reference

UI USB4 Specification section 13-1.

10.1.5.2 Requirement

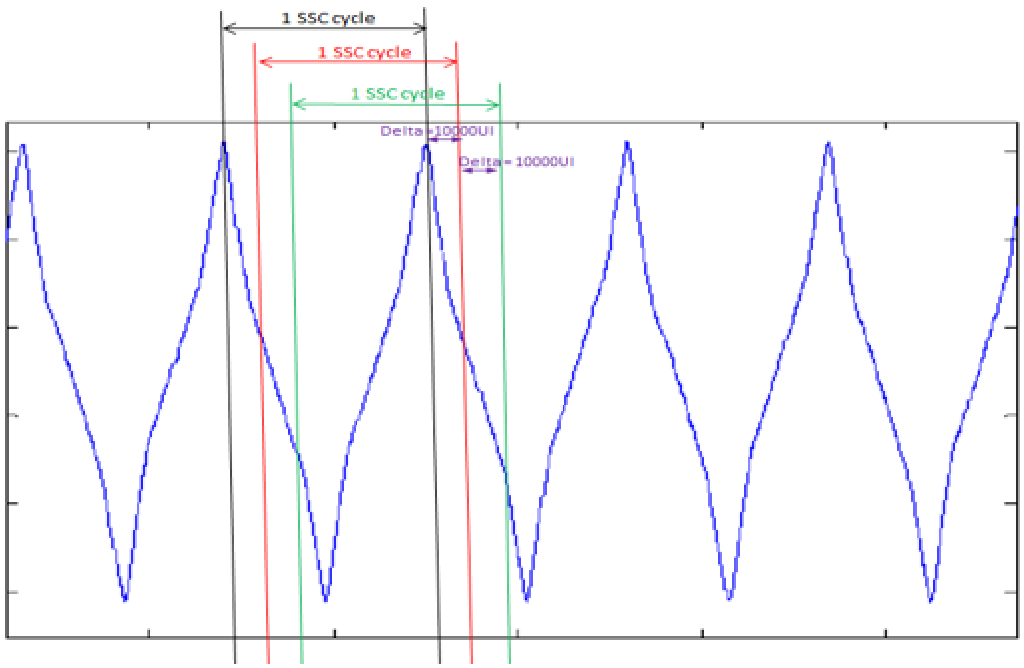
97.1348ps ≤ Average Unit Interval ≤ 97.2420ps.

10.1.5.3 Test Objective

Confirm that the Average Unit Interval under all conditions does not exceed minimum or maximum limits of the USB4 Specification.

10.1.5.4 Test Method

1. Choose a supported USB4 Gen2 10.3125GHZ speed.
2. Configure DUT transmitter to output PRBS31 on all lanes with SSC turned on.
3. The cables from the plug test fixture to the scope shall be de-embedded.
4. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 27Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 27Mpts. The evaluated record length shall be 337.5µs per channel.
    - No CDR, no average and no interpolation to be used.
    - Adjust vertical scale to fit signal into scope screen.
    - Scope BW shall be as specified in 6.1.
5. Use mathematical analysis to measure the Average Unit Interval over a window at the size of one SSC cycle, determined by the SSC_Down_Spread_Rate.



6. Measure Average Unit Interval over different windows that uniformly cover the scope capture over more than 10 SSC cycles with 10000 UI window jump.
7. If Max Average Unit Interval > 97.2420ps **or** Min Average Unit Interval < 97.1348ps then **Fail**.
8. Repeat the test for all remaining USB4 lanes.

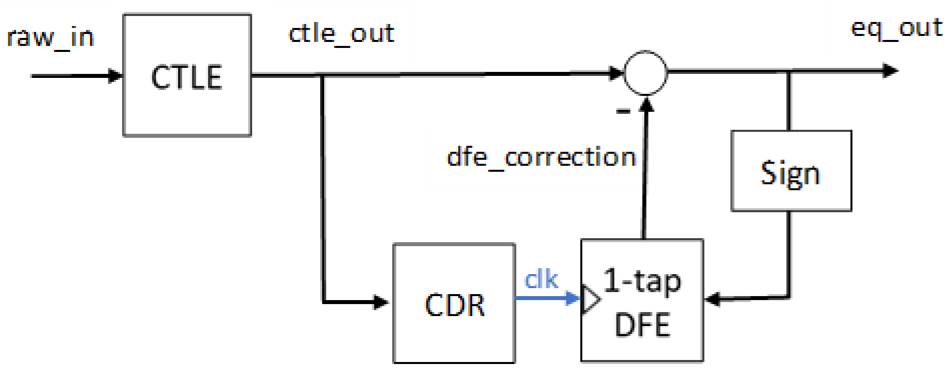
10.2 Gen2 and Gen3 Router Assembly Receiver Compliance

Two items different from the section 4 above:

1. SSC profile that shall be added from the pattern generator is as following:
    - SSC with modulation wave shape triangle.
    - Modulation frequency for both Gen2 and Gen3 is 36KHz.
    - Spread deviation from +400ppm up to -5400ppm.
2. Additional receiver "Case 2a" test setup shall be supported, addressing optical interconnects with limiting modules. Case 2a setup is identical to Case 2 setup described in section 4.2.2 above, except that the passive cable is replaced with worst case limiting optical cable (corresponding to the USB Type-C Specification). A receiver shall operate at BER of 1E-12 or lower with neither Forward Error Correction nor Pre-Coding applied when a stressed signal is driven at its input.

11 Appendix F – Applying DFE method

USB4 eye measurements at TP3 shall be performed using reference RX equalization function comprised of CTLE and DFE, as described in figure bellow. The eye shall be characterized after convolving the raw input signal with the CTLE function and subtracting the DFE correction implemented as constant voltage timed by the CDR clock aligned to the CTLE output. The DFE correction extends as a constant voltage from the mid-point between the UI clock and the previous clock to the mid-point between the UI clock and the next clock. In this way, the DFE correction is applied half a UI before the data sampling and changes again half a UI after the data sampling.



Waveforms example:

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12 Appendix G – Transmitter Preset calibration

12.1.1 Preset Calibration

12.1.1.1 Reference

USB4 Specification Table 3-5.

12.1.1.2 Objective

Find and Burn the optimized preset for the platform.

12.1.1.3 Method

1. Connect the DUT according to Figure 2.
2. Choose a USB4 speed to start with.
3. Configure DUT transmitter to output PRBS15, preset 0 on all lanes with SSC turned on.
4. The cables from the plug test fixture to the scope shall be de-embedded.
5. Measurement shall be done with a reference CDR modeled by a 2nd order PLL response which drives High-Pass-Filter (HPF) rejection mask with 3dB bandwidth at 5MHz and damping factor of 0.94; no average and no interpolation to be used. Scope BW shall be as specified in 6.1.
6. Capture the waveform and process it with the oscilloscope:
    - Sampling Rate ≥ 80GSa/s.
    - Evaluate 40Mpts per channel when using 80GSa/s. For higher sample rate use memory depth in the same ratio to 40Mpts. The evaluated record length shall be 500µs per channel.
    - Pattern length -> Periodic.
    - Jitter separation method shall be suitable for cross talk on signal.
    - Adjust vertical scale to fit signal into scope screen.
    - Referenced to 1E-13 statistics.
7. Capture DDJ results for lane 0.
8. Repeat the test for all remaining USB4 transmit presets until Preset 15 of the Transmitter Equalization Preset Table above.
9. Repeat the test for all remaining USB4 lanes.
10. For each lane, choose and Burn the preset that provides minimum DDJ.
11. Repeat the procedure above for all supported USB4 speeds.

13 Appendix H – Mechanical

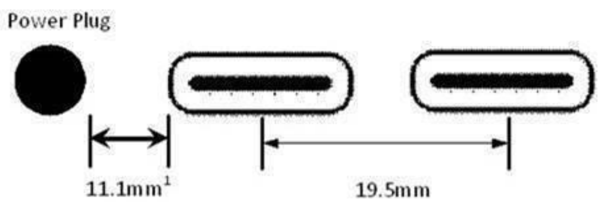
13.1.1 USB Type-C Receptacle minimum spacing

USB4 Electrical CTS is performed by using Test Fixtures.

While performing the tests the battery life of the Router Assembly is reduced.

USB4 test fixtures enable charging the Router Assembly via the micro-controller unless it's not enabled in the tested Router Assembly.

For USB Type-C Receptacle that not support charging, there is a need for minimum spacing between USB Type-C Receptacle to the next USB Type-C connector and to the next Power Plug. The following pictures illustrating the needed minimum spacing.



1. Spacing includes plastic molding of power plug cable